MIM WEBINARS
AN IN-MEMORY COMPUTING SERIES

Next Talk: 07/June/2021, 4-5:30pm CET

UPMEM DPU, ARCHITECTURE OF THE FIRST COMMERCIALLY AVAILABLE PIM SYSTEM

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In this talk, we will present UPMEM PIM-DRAM, a disruptive, efficient, and scalable Processing In-Memory engine that will significantly contribute to this progress, covering most needs for upcoming digital intelligence and service, unleashing the horizon and impact of application developers. We will have a look into the architecture of UPMEM DPU, its supported programming paradigms and some examples of application benefiting from UPMEM DPU and their achieved speed-up. We then delve deeper into different aspects of logic on DRAM and close by presenting a perspective on what is likely and feasible to implement on DRAM (and what is not!), as well as the challenges of integrating a PIM memory into a legacy system.

More information about the event and the speaker:
https://www.ict.tuwien.ac.at/staff/taherinejad/MiM/