ATM Technology

Asynchronous Transfer Mode
Principles, ATM Layering, AAL, Signaling

Agenda

- Introduction
- ATM Reference Model
- Physical Layer
- ATM Layer
- ATM Switching Details
- ATM Adaptation Layer
- ATM Signaling and Addressing

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Introduction

- **In 1986 the CCITT (now ITU-T) adopted ATM as background technology for B-ISDN**
  - B-ISDN intended to replace several widespread incompatible technologies
    - integration of voice, video and data
  - However, the data communications industry tried to push IEEE 802.6 - DQDB
  - remark: N-ISDN is based on synchronous TDM
- **First developments in 1988 by CCITT**
- **ATM Forum established in 1991**
  - Focuses on implementation rules for ATM
  - Most members were switch manufacturers

What is ATM?

- **ATM**
  - Asynchronous Transfer Mode
  - Based on asynchronous TDM
    - hence buffering and address information is necessary
- **Cell switching technology**
  - based on store-and-forward of cells
  - a form of packet switching
  - connection oriented type of service with virtual circuits
- **ATM cell**
  - small packet with constant length
  - 53 bytes long
    - 5 bytes header + 48 bytes data

What is Asynchronous Transfer Mode?

Synchronous TDM

(+) Constant delay (good for voice)

(+) Protocol transparent

(–) Fixed channel assignment (might be uneconomic)

(–) Trunk bandwidth = sum of channel speeds (expensive)

Asynchronous TDM

(–) Variable delay (variable frame sizes)

(+/~) Fairly protocol transparent

(+) Flexible channel assignment (using addresses)

(+) Trunk bandwidth = average of channel speeds

Asynchronous Transfer Mode (ATM)

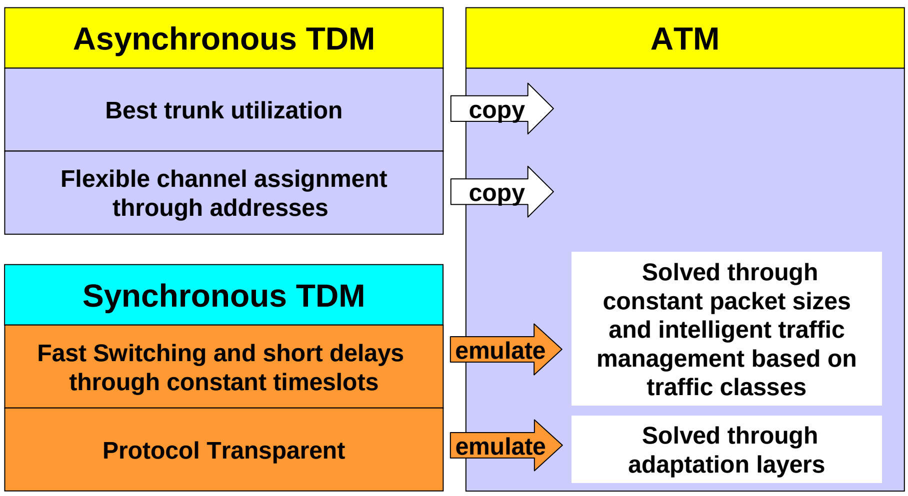
(+) Bounded delay through fixed cell sizes (53 bytes) and intelligent traffic management based on different traffic classes

(+) Protocol transparent through higher layers (CPCS and SAR)

(+) Flexible channel assignment using addresses (VPI/VCI)

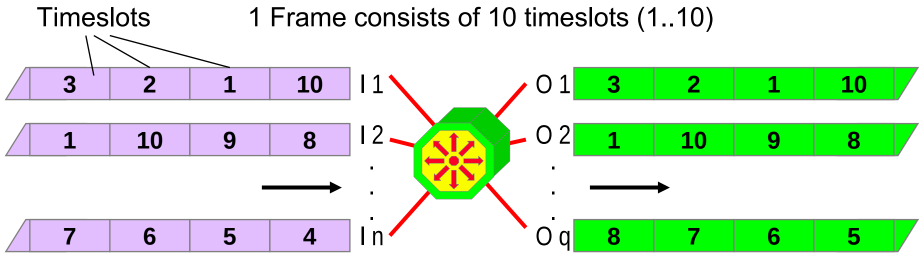
(+) Trunk bandwidth according average channel speeds

Design Ideas



Synchronous Transfer Mode

Example:
1 Frame consists of 10 timeslots (1..10)

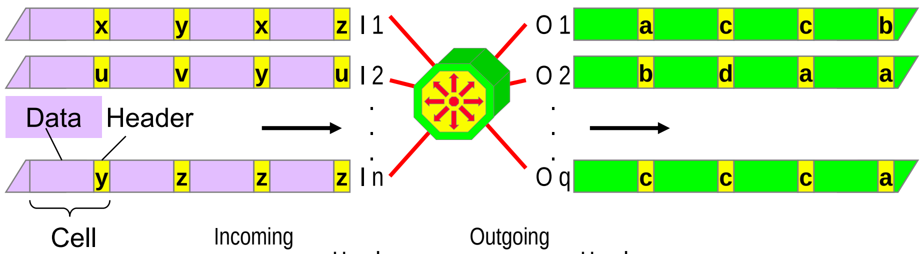


Incoming Connection	Timeslot	Outgoing Connection	Timeslot
I1	1	O2	8
	2	O7	3
	3	O1	3
	4	O4	5
......		......	

Fixed timeslot allocation !

Asynchronous Transfer Mode

Cell header: addressing information for routing and dynamic bandwidth allocation

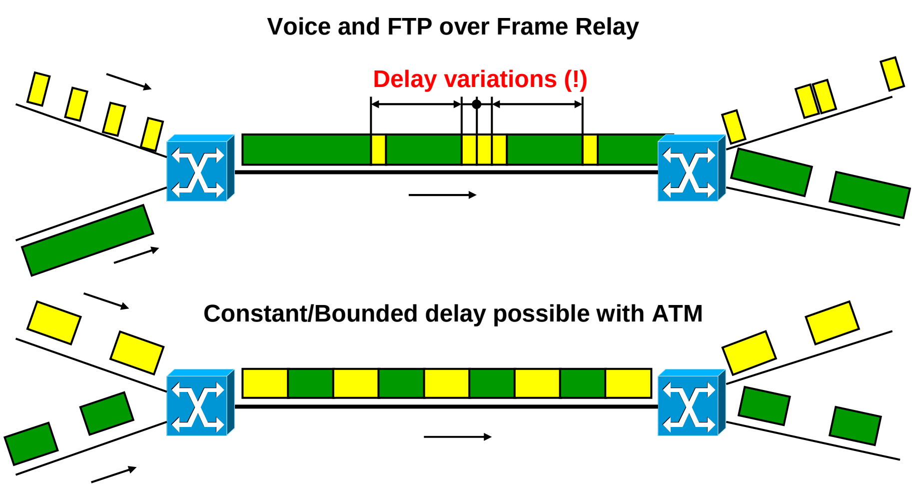


Incoming Connection	Header	Outgoing Connection	Header
I1	v	O2	a
	w	O7	b
	x	O1	c
	y	O4	d
......		......	

Why Cells?

- **Cell switching technology allows**
  - Forwarding of cells in hardware
    - Hence very fast
  - Predictable and bounded delay for a given cell
    - It is still variable!
  - Quality of Service (QoS)
    - With specific strategies like admission control, QOS routing, traffic shaping, traffic policing, cell scheduling, ….
  - Integration of voice, video and data
    - Real-time traffic and non real-time traffic on the same network infrastructure

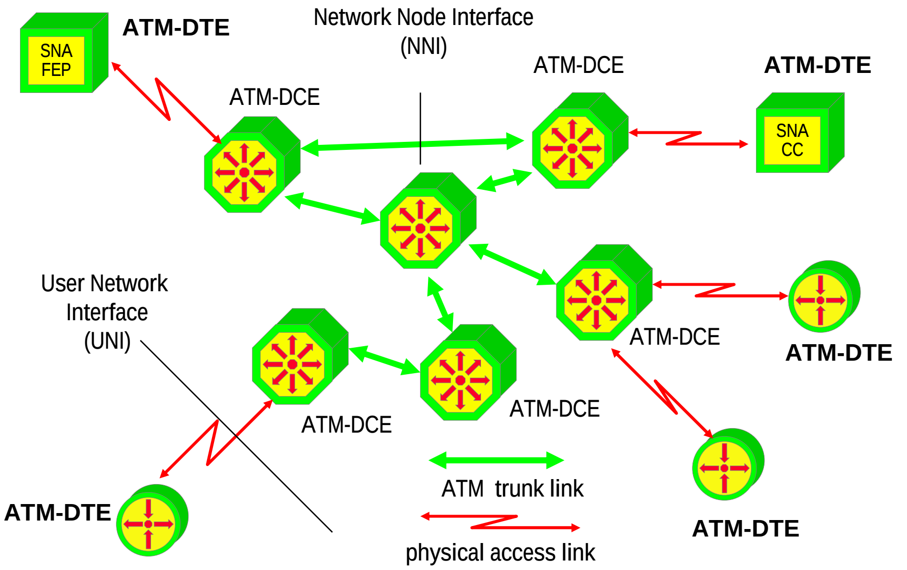
Cell Switching and Jitter



ATM Technology

- **WAN service and (campus area network service)**
  - Based on virtual circuit technique
  - Connection oriented, enables charging for carriers and providers
  - Sequencing of cell stream is guaranteed but no error recovery is done for damaged cells
  - One single technology to cover both WAN and LAN (MAN) aspects
- **Standardized interface definitions**
  - User Network Interface (UNI)
    - between ATM-DTE and ATM-DCE
  - Network Node Interface (NNI)
    - between ATM-DCE and ATM-DCE

ATM Basic Topology



ATM Virtual Circuits

- **Virtual circuit technique used**
  - For statistically multiplexing many logical conversations over a single physical transmission link
- **End systems (ATM-DTE) use virtual circuits for delivering data to the ATM network and vice versa**
- **Virtual circuits appear to end systems as transparent transport pipes**
  - Logical point-to-point connections

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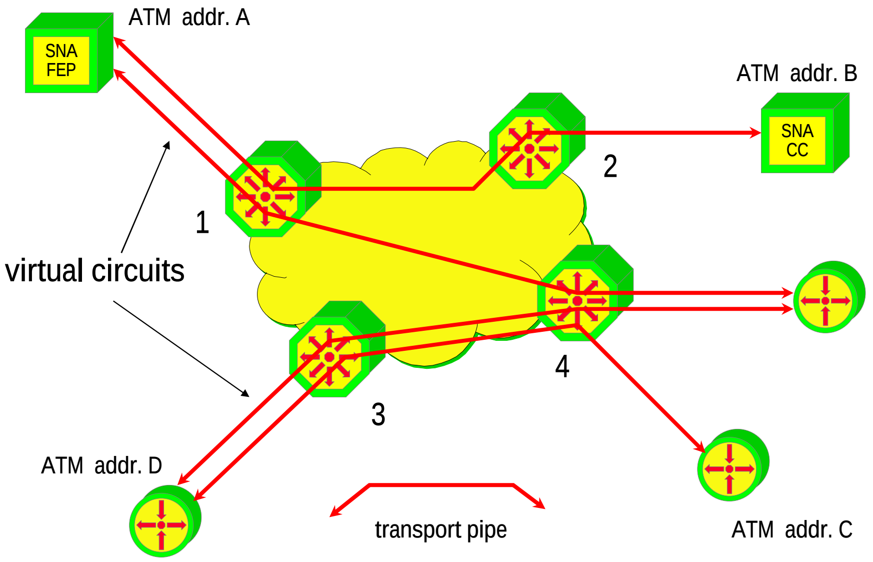
ATM Virtual Circuits



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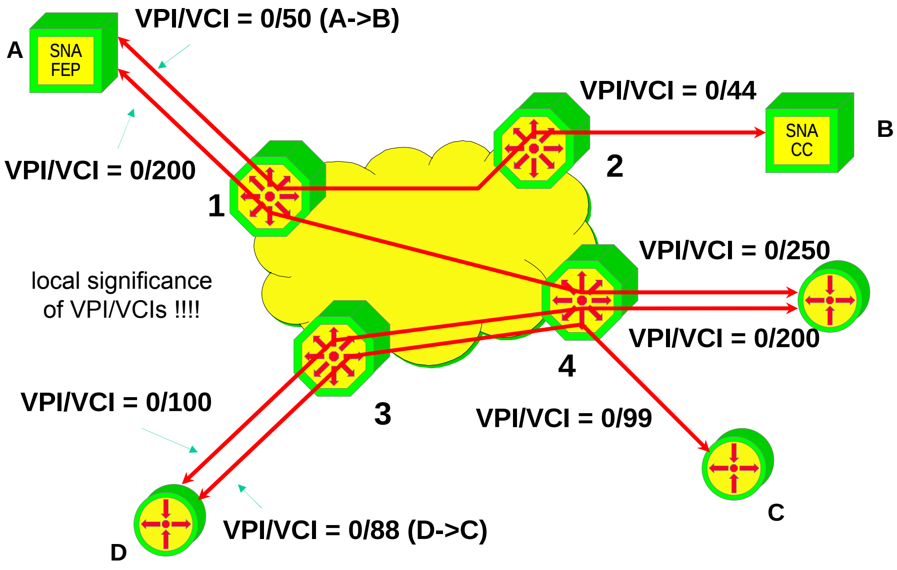
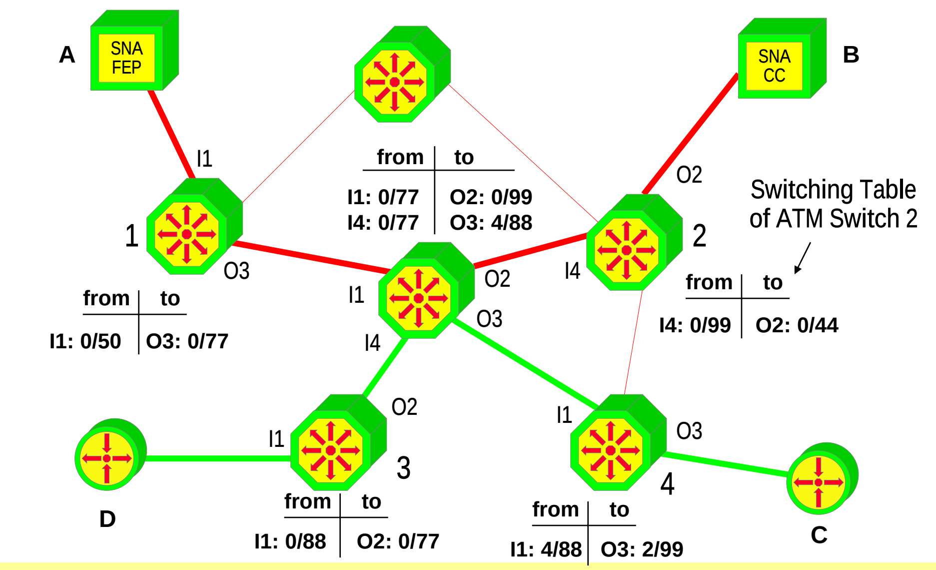
ATM VPI / VCI

- **Virtual circuits (VCs) are identified using VPI / VCI numbers**
  - Virtual Path Identifier / Virtual Channel Identifier
  - Only locally significant
- **Two kinds of virtual circuits**
  - Permanent virtual circuits (ATM-PVC) established in advance by service provider
  - Switched virtual circuits (ATM-SVC) established on demand by user through signaling procedure

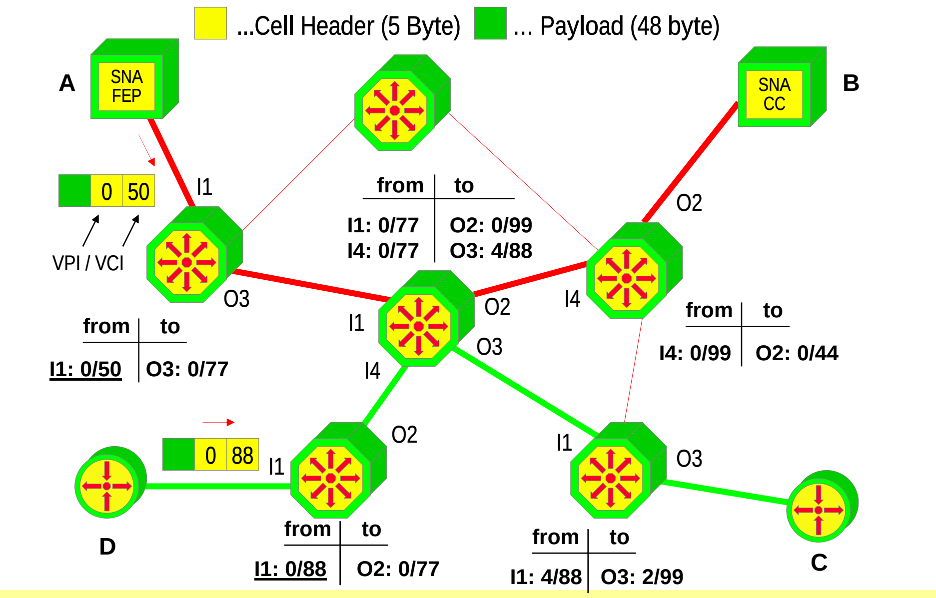
VPI / VCI



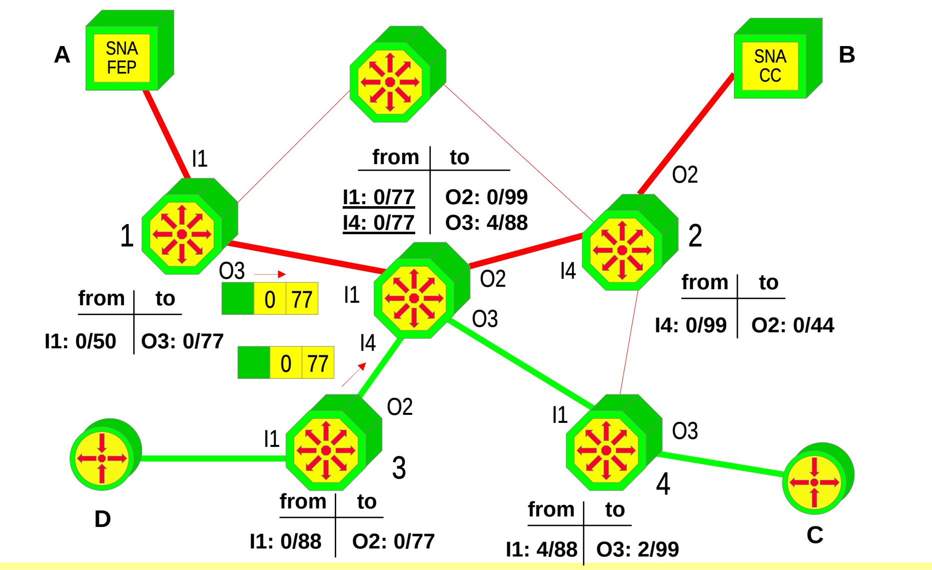
ATM Switching Tables



Cell Forwarding / Label Swapping 1



Cell Forwarding / Label Swapping 2



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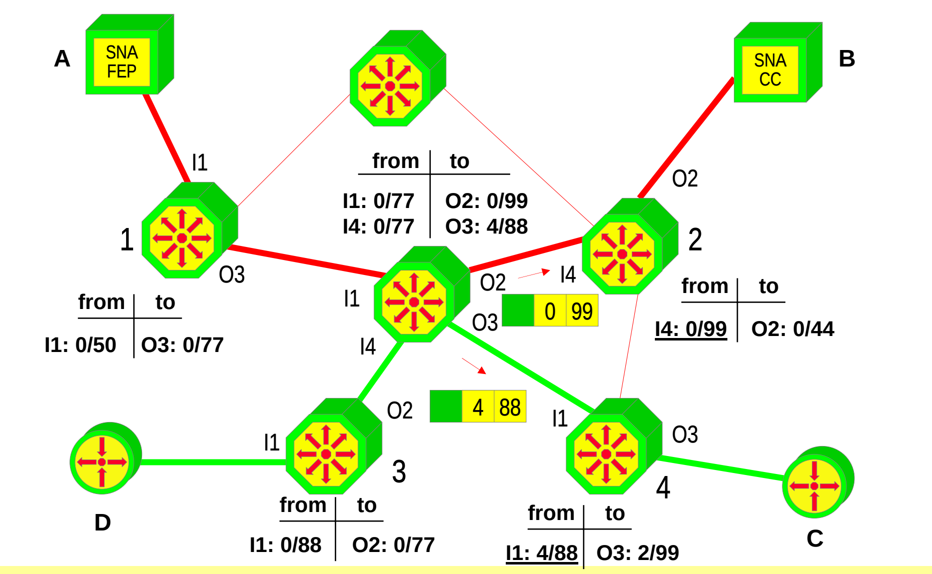
Cell Forwarding / Label Swapping 3



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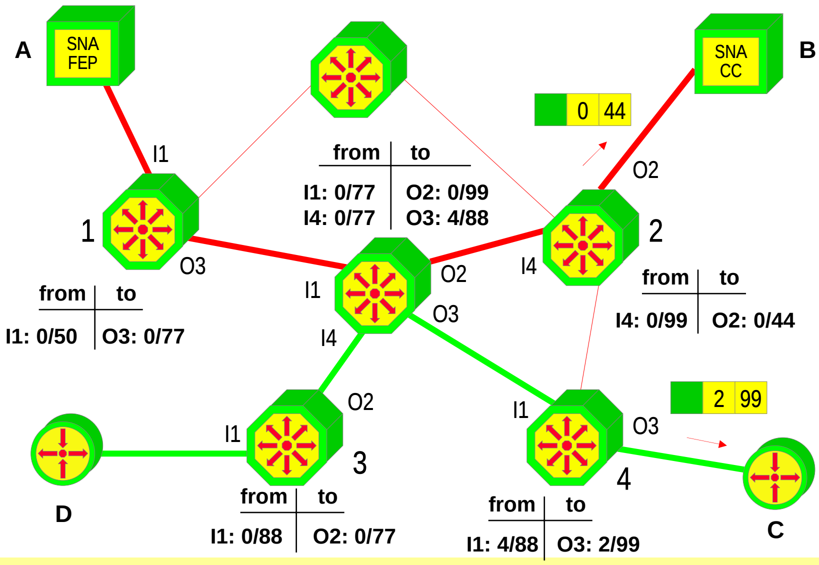
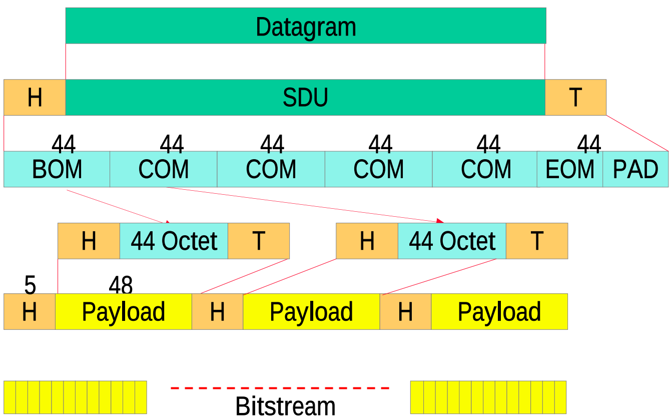
Cell Forwarding / Label Swapping 4



Segmentation Principle

- **Cells are much smaller than data packets**
  - Segmentation and Reassembly is necessary in ATM DTEs (!!!)
  - ATM DCEs (ATM switches) are not involved in that



ATM Usage

- Public and private networks
  - LAN, MAN, WAN
- Backbone high-speed networks
  - Public (Telco's) or private
- Original goal: World-wide ATM network
  - But Internet technology and state-of-the art Ethernet are more attractive today
- New importance as backbone technology for mobile applications
  - Cellular networks for GSM, GPRS, UMTS, ...

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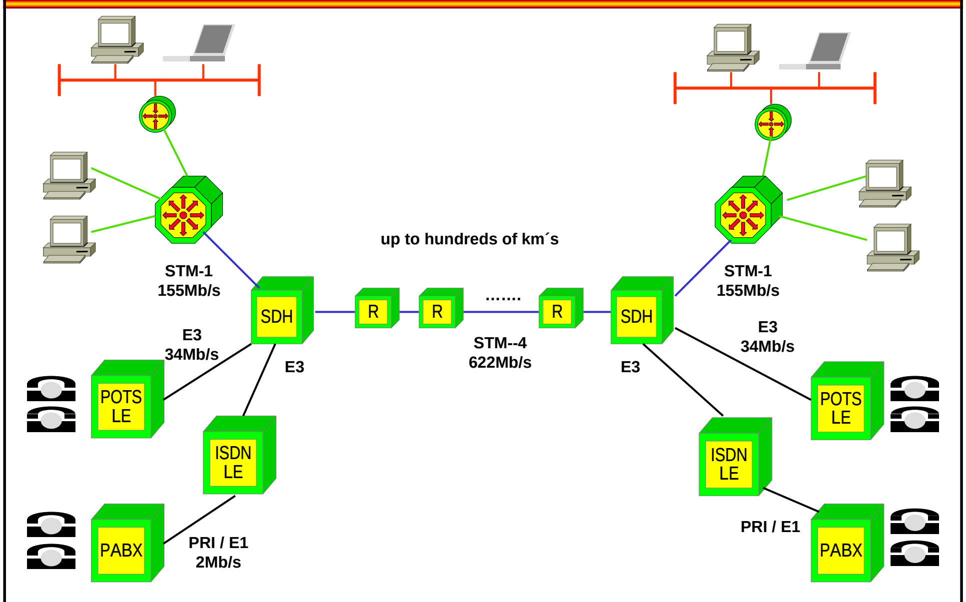
ATM as WAN Technology based on SDH



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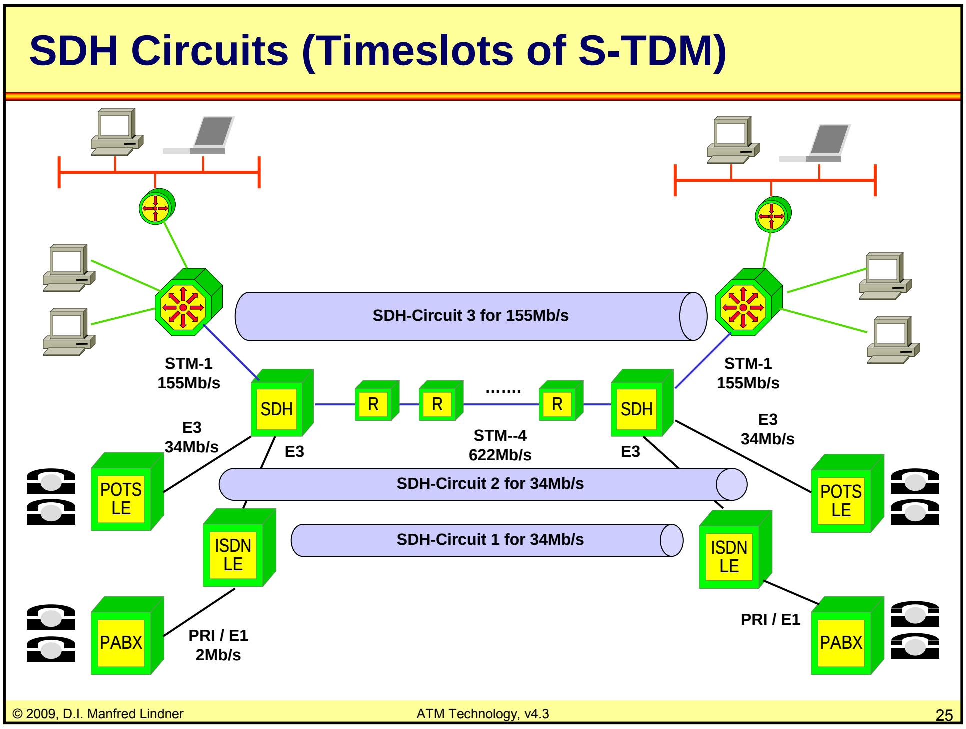
SDH Circuits (Timeslots of S-TDM)
SDH-Circuit 3 for 155Mb/s
STM-1
155Mb/s
SDH
R
R
....
R
SDH
STM-1
155Mb/s
E3
34Mb/s
E3
STM--4
622Mb/s
E3
E3
34Mb/s
POTS
LE
SDH-Circuit 2 for 34Mb/s
ISDN
LE
SDH-Circuit 1 for 34Mb/s
ISDN
LE
POTS
LE
PABX
PRI / E1
2Mb/s
PRI / E1
PABX
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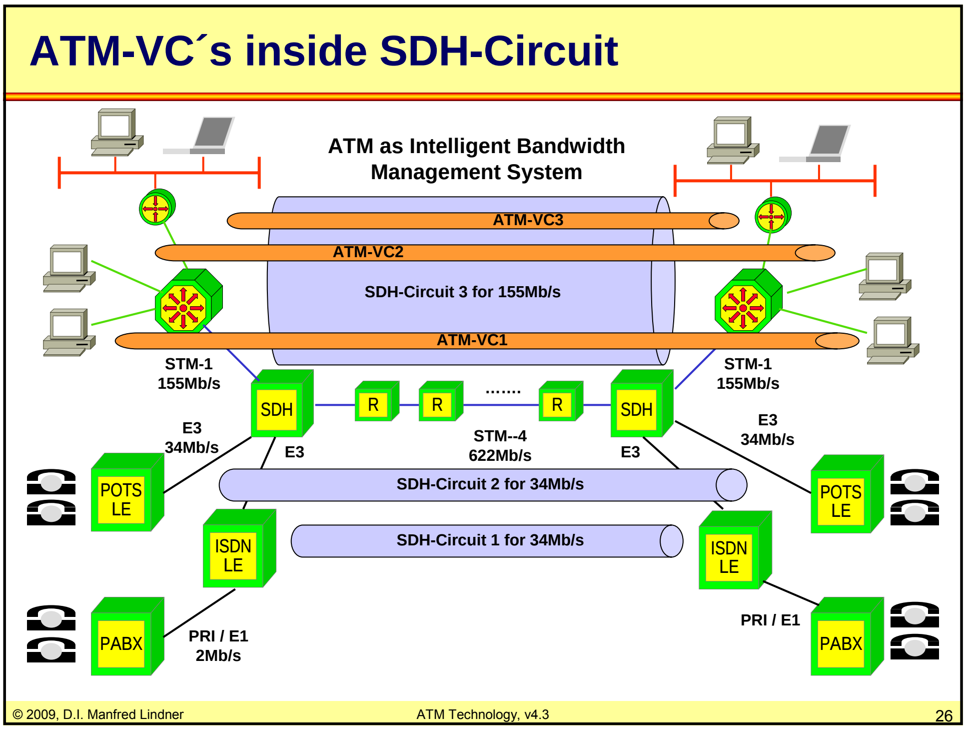
ATM-VC´s inside SDH-Circuit
ATM as Intelligent Bandwidth
Management System
ATM-VC3
ATM-VC2
SDH-Circuit 3 for 155Mb/s
ATM-VC1
STM-1
155Mb/s
SDH
R
R
....
R
SDH
STM-1
155Mb/s
E3
34Mb/s
E3
STM--4
622Mb/s
E3
E3
34Mb/s
POTS
LE
SDH-Circuit 2 for 34Mb/s
ISDN
LE
SDH-Circuit 1 for 34Mb/s
ISDN
LE
POTS
LE
PABX
PRI / E1
2Mb/s
PRI / E1
PABX
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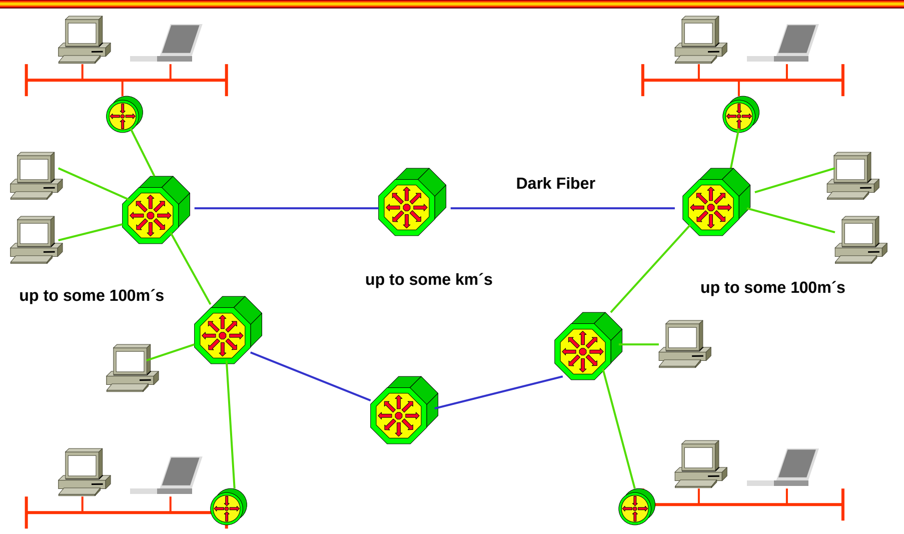
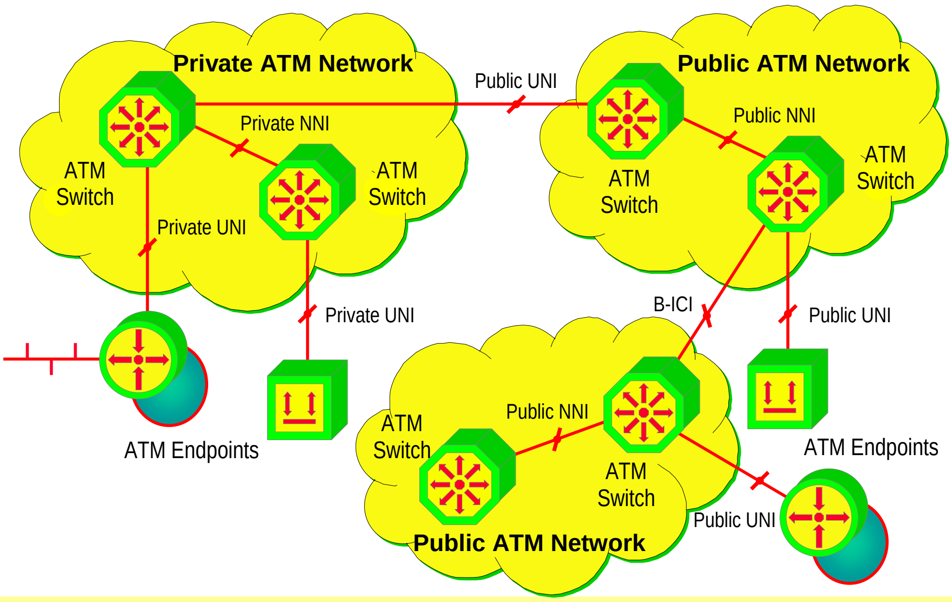
ATM as LAN/ MAN Technology based on Dark Fiber



Standardization Responsibilities

- **Private ATM networks: ATM Forum**
  - Private UNI
    - Addressing similar to OSI NSAP addresses
  - Private NNI
    - Dynamic routing based on Link State Technique (PNNI)
- **Public ATM networks: ITU-T**
  - Public UNI
    - Addressing based on E.164 addressing schema
  - Public NNI
    - Static routing

UNI and NNI Types



Network Node Interface (NNI)

- **NNI-ISSI (Public NNI)**
  - ISSI - Inter Switch System Interface
  - Used to connect two switches of one public service provider
- **NNI-ICI (B - ICI)**
  - ICI - Inter Carrier Interface
  - Used to connect two ATM networks of two different service providers
- **Private NNI**
  - Used to connect two switches of different vendors in private ATM networks

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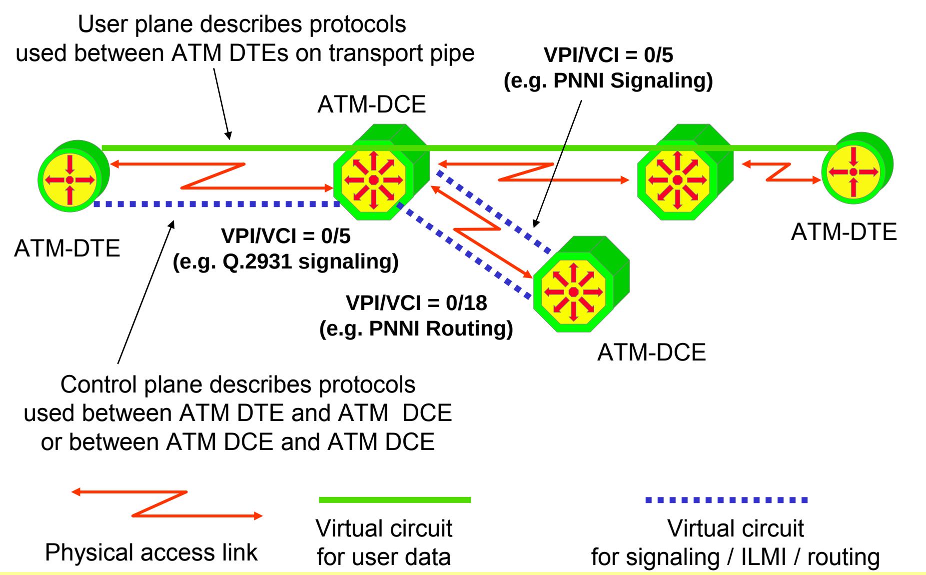
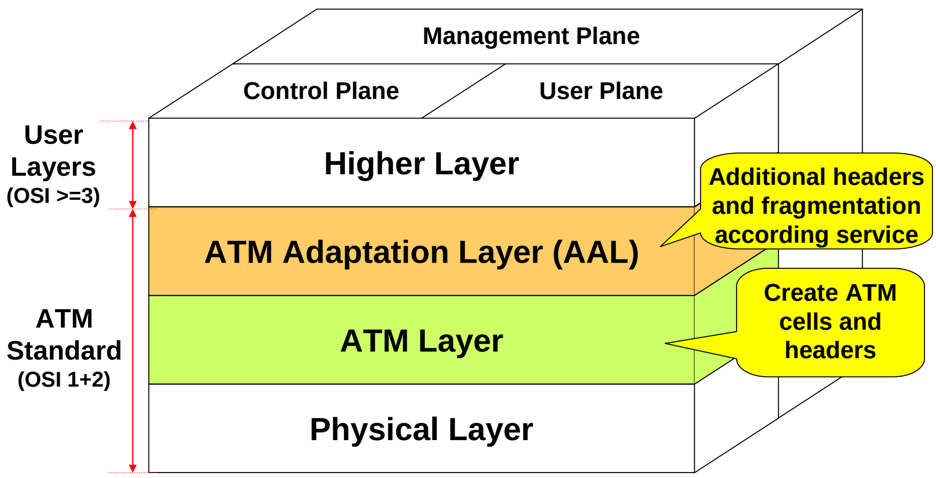
ATM Reference Model

- **User Plane**
  - Provides for transfer of information
- **Control Plane**
  - Call control (Signaling) , connection control, PVC management, interim local management interface (e.g. ILMI)
- **Management Plane**
  - Layer management
    - e.g. meta-signaling, layer specific Operation and Maintenance (OAM) information flow
  - Plane management
    - Management functions related to the whole system
    - Coordination between all planes

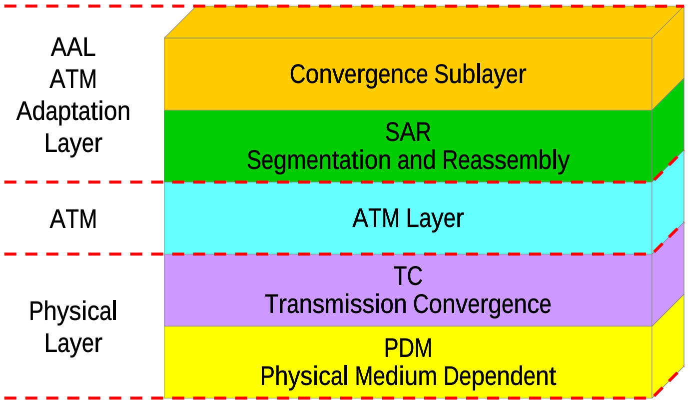
Control Plane <-> User Plane



ATM Reference Model Overview



ATM Layers



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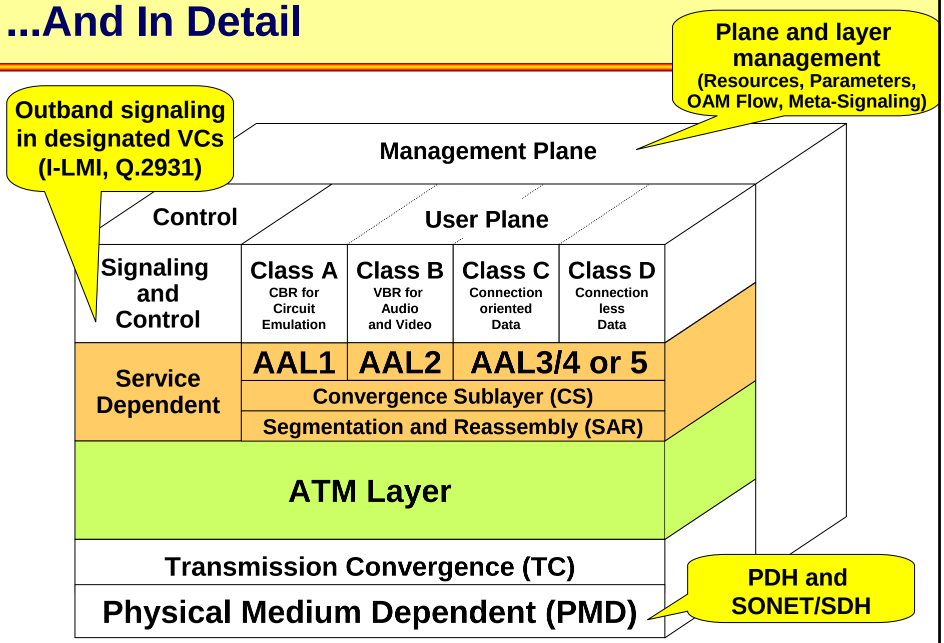
...And In Detail



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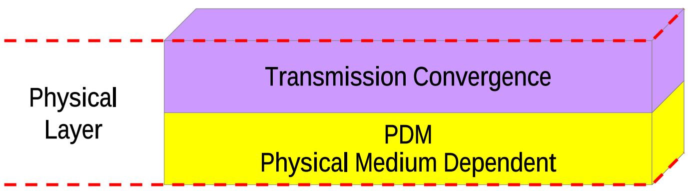
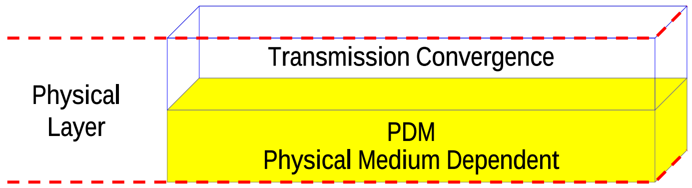
Physical Layer



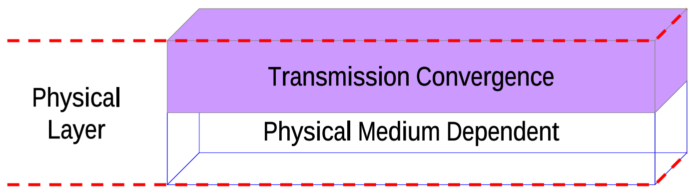
- Subdivided into PMD Layer and Transmission Convergence Sublayer
- Allows easy exchange of the physical medium
  - can be adapted to newest transmission technology

PMD Sublayer



- Bit transmission capability, bit alignment (Bit-synchronization, Clock-synchronization)
- Line coding
- Electrical/optical conversion
- A lot of different media have been specified in various standard documents

Transmission Convergence Sublayer



- Adaptation to different transmission systems (i.e. SDH, PDH, SONET, dark fiber)
- Generation and recovery of the transmission frame (e.g PDH, SDH framing)
- Transmission frame adaptation
- Cell delineation (recovery of cell boundaries -> frame synchronization)
- HEC (Header Error Check) generation
- Cell rate decoupling (user cell rate versus needed synchronous cell rate)

Some Available Interfaces

Standard	Speed	Medium	Comments	Encoding	Connector	Usage
SDH STM-1	155,52	Coax	75 Ohm	CMI	BNC	WAN
PDH E4	139,264	Coax	75 Ohm	CMI	BNC	WAN
PDH DS3	44,736	Coax	75 Ohm	B3ZS	BNC	WAN
PDH E3	34,368	Coax	75 Ohm	HDB3	BNC	WAN
PDH E2	8,448	Coax	75 Ohm	HDB3	BNC	WAN
PDH J2	6,312	TP/Coax	110/75 Ohm	B6ZS/B8ZS	RJ45/BNC	WAN
PDH E1	2,048	TP/Coax	120/75 Ohm	HDB3	9pinD/BNC	WAN
PDH DS1	1,544	TP	100 Ohm	AMI/B8ZS	RJ45/RJ48	WAN
SDH STM-4	622,08	SM fiber		SDH	SC	LAN/WAN
SDH STM-1	155,52	SM fiber		SDH	ST	LAN/WAN
SDH STM-1	155,52	MM fiber	62,5 um	SDH	SC	LAN/WAN
SDH STM-4	622,08	SM fiber		NRZ	SC (ST)	LAN
SDH STM-4	622,08	MM (LED)		NRZ	SC (ST)	LAN
SDH STM-4	622,08	MM (Laser)		NRZ	SC (ST)	LAN
SDH STM-1	155,52	UTP5	100 Ohm	NRZI	RJ45	LAN
SDH STM1	155,52	STP (Type1)	150 Ohm	NRZI	9pinD	LAN
Fiber Channel	155,52	MM fiber	62,5 um	8B/10B		LAN
TAXI	100	MM Fiber	62,5 um	4B/5B	MIC	LAN
SONET STS1	51,84	UTP3		NRZI	RJ45	LAN
ATM25	25,6	UTP3		NRZI	RJ45	LAN

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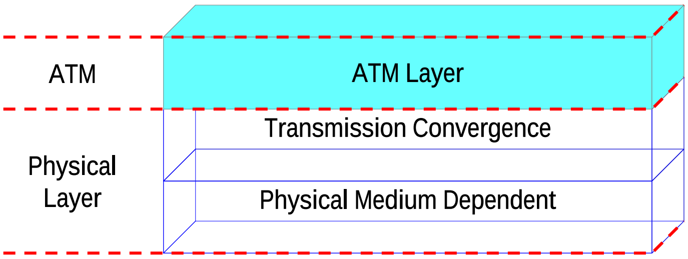
ATM Layer

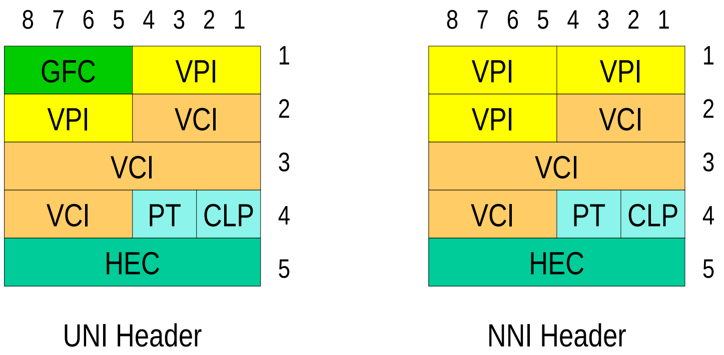


- Transparent transfer of ATM cells
  - Independent of Physical Layer
- Multiplexing/demultiplexing of cells with different VCI/VPI
- Translation of VPI/VCI (label swapping)

ATM Layer

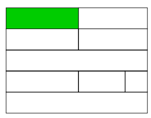
- Extraction/addition of cell header at destination/source
- Switching of cells with Label Swapping
- Error management - OAM cells (F4/F5)
  - OAM = Operation And Maintenance
- Meta signaling
- QoS negotiation and control
- Traffic shaping
  - Ensures that nodes do not exceed their committed QoS parameters
- Flow control (in case of ABR)

Cell Format



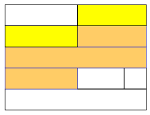
- **Two slightly different formats**
  - UNI … 8 bits for VPI
  - NNI … 12 bits for VPI

Cell Format



- **Cell Size**
  - 53 byte
  - 5 byte header and 48 byte payload
- **Generic Flow Control (GFC)**
  - Not contained in the NNI header -> not transported through the network
  - Local significance between ATM endpoint and switch
  - Not used!
    - Today, special flow-control cells are used instead

Cell Format



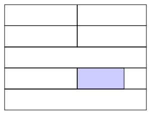
- **VPI and VCI**
  - VPI - Virtual Path Identifier
  - VCI - Virtual Channel Identifier
  - VPI/VCI identifies the virtual connection
    - Similar function as the X.25 logical channel identifier or the Frame Relay DLCI
  - Reserved values used for
    - Signaling
    - Operation and maintenance
    - Resource management
  - Idle cells - VPI/VCI set to “0”
    - Used within framed structures like SDH and PDH
    - Not needed within unframed structures like TAXI
    - User data is 0x55 pattern (0101 0101)

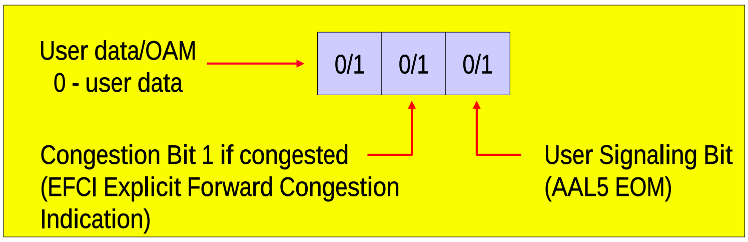
Reserved VPI/VCI Values

VPI	VCI	Function
0	0- 15	ITU-T
0	16 - 31	ATM Forum
0	0	Idle Cell
0	3	Segment OAM Cell (F4)
0	4	End-to-End OAM Cell (F4)
0	5	Signaling
0	16	ILMI
0	17	LANE
0	18	PNNI

Cell Format

- **Payload Type (3 bits)**
  - Used by AAL5 to flag end of block
  - Used to signal congestion





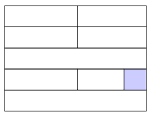
Value	OAM Type
100	OAM link associated cell
101	OAM end-to-end cell
110	Resource management cell
111	Reserved

(OAM = Operation, Admin, Management)

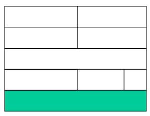
RM cell (Flow control in ABR)

Cell Format

- **Cell Loss Priority (CLP)**
  - Similar to the DE-bit in Frame Relay
  - Normal transmission: CLP = 0
  - Low priority, might be discarded: CLP = 1



- **Header Error Check (HEC)**
  - Allows the correction of single-bit errors in the cell header or
  - Allows the detection of most multi-bit errors
  - Also used for determining cell boundaries on some types of physical link connection
    - ITU-T standard I 4.321
    - Switch must find 6 successive hits at any time



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Switching Principles

- Each virtual connection is represented by two IDs
  - Virtual Path Identifier (VPI)
  - Virtual Channel Identifier (VCI)
- Switching is done by using table pointers
  - Table of VPIs relating to each physical link
  - Table of VCIs for each terminating VP
- VP switch
  - Only changes the VPI of a cell, used for VC aggregation on intermediate switch(es)
- VC switch
  - Changes the VPI and VCI

Switching Tables

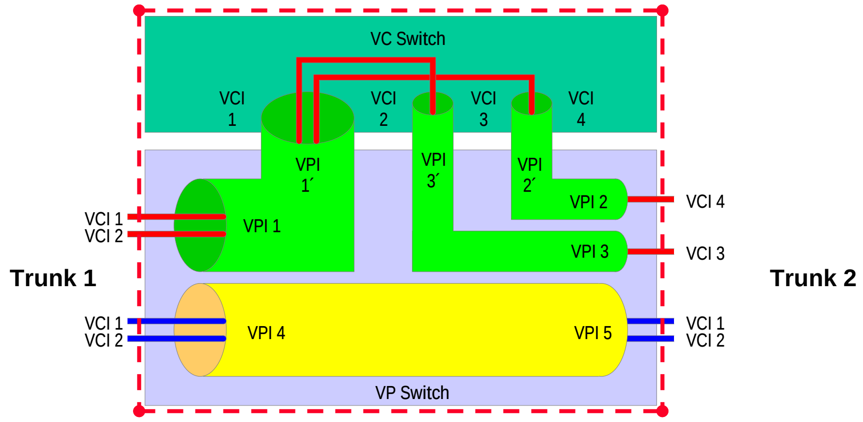


Table for Trunk 1

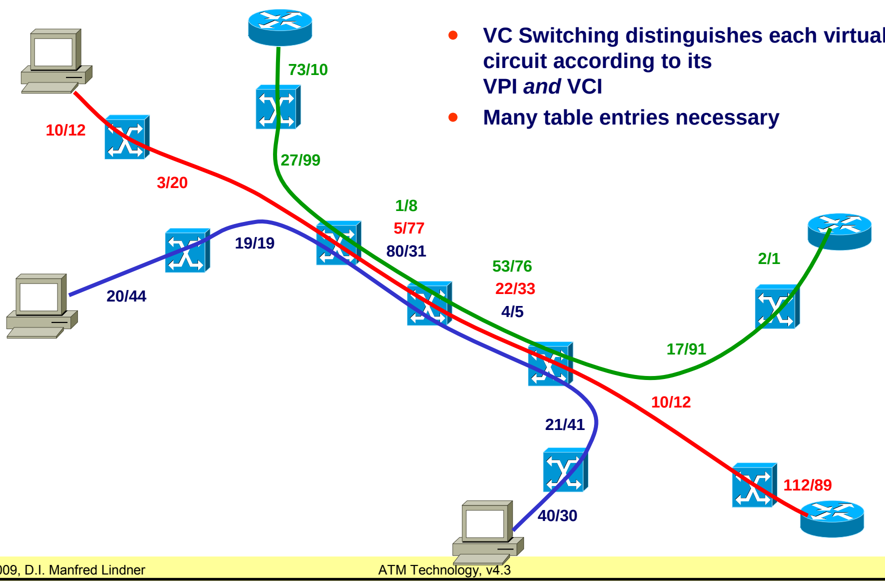
IN VP	Outbound Trunk	Outbound VP	Outbound VC
1	Pointer		
.....			
.....			
4	2	5	xx

Table for VPI 1

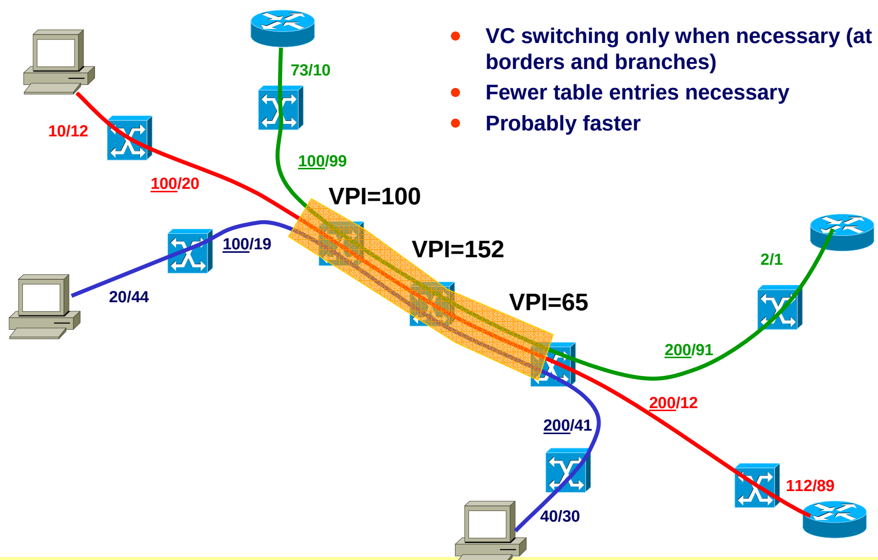
IN VC	Outbound Trunk	Outbound VP	Outbound VC
1	2	3	3
2	2	2	4
.....			
.....			

VC Switching

- **VC Switching distinguishes each virtual circuit according to its VPI *and* VCI**
- **Many table entries necessary**



VP and VC Switching

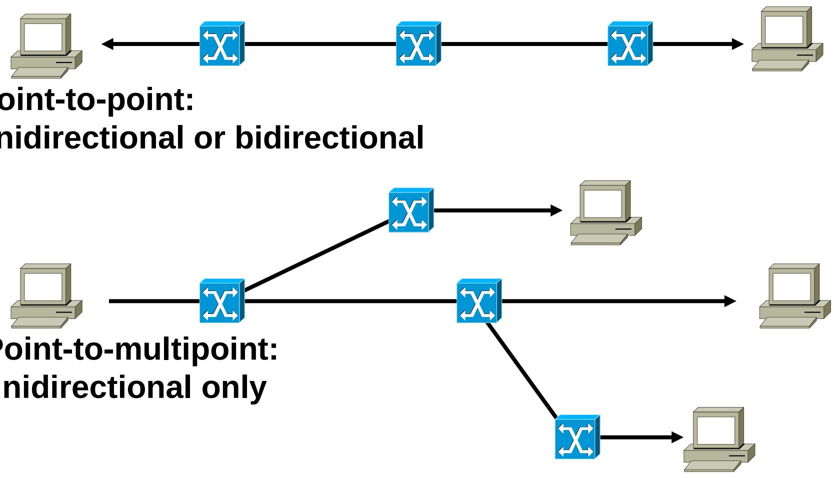


- VC switching only when necessary (at borders and branches)
- Fewer table entries necessary
- Probably faster

Network Characteristics

- **Connection oriented network**
  - Connection has to be established prior to data transfer
    - Permanent virtual connections - PVC
    - Switched virtual connections - SVC
  - Eases charging of customers
- **Cell sequence integrity**
  - Sequencing of cell stream is guaranteed due to connection oriented operation
  - Hence receiver has no need to resequence

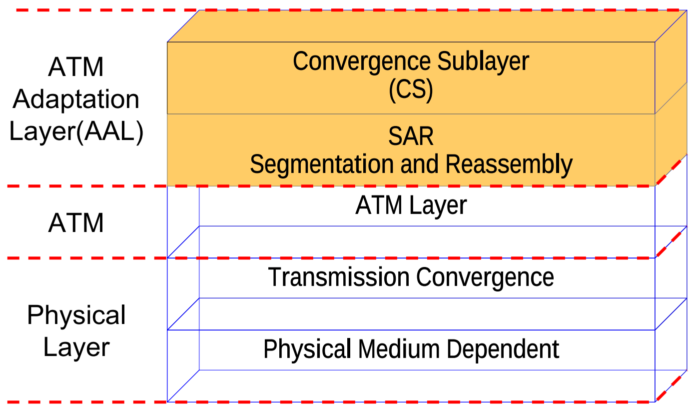
ATM Connection Types



Agenda

- Introduction
- ATM Reference Model
- Physical Layer
- ATM Layer
- ATM Switching Details
- <u>ATM Adaptation Layer</u>
- ATM Signaling and Addressing

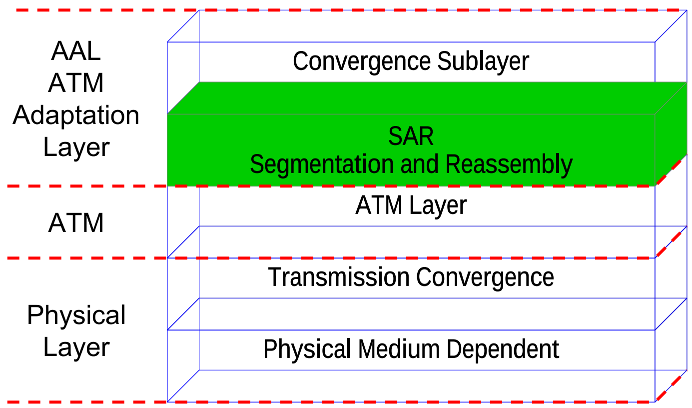
ATM Adaptation Layer



Adaptation Layers

- **ATM only provides bearer service**
- **ATM cannot be used directly**
- **Applications must use adaptation layers to access the ATM layer**
- **Adapts different kind of information streams to ATM**
  - Constant Bit Rate (CBR), Variable Bit Rate (VBR)
  - Connection-oriented Data (CO-D), Connection-less Data (CL-D)
- **Consist of SAR and CS**
  - Part of ATM-end-systems (DTE´s) only
  - Transparent for ATM-switches (DCE´s)

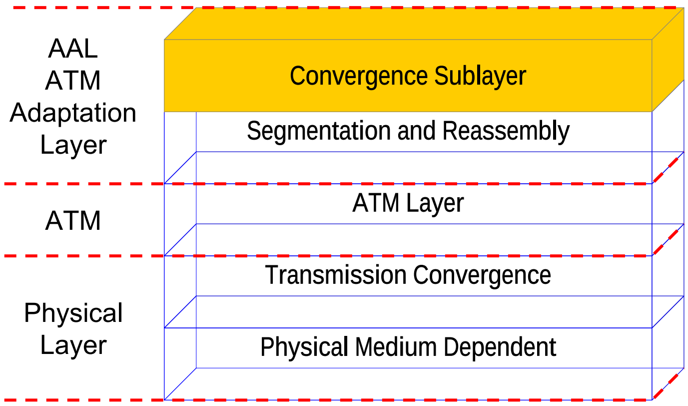
Segmentation and Reassembly Sublayer



SAR Layer

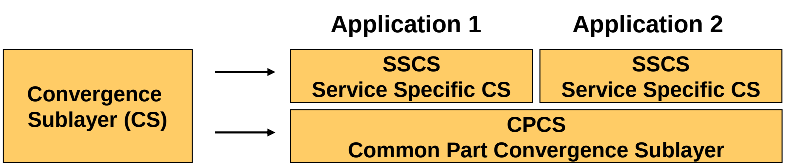
- **Segmentation and Reassembly Sublayer**
  - Used to "fill" information into ATM cell payload
  - Segmentation in and reassembly from ATM cells

Convergence Sublayer



Convergence Sublayer

- **Convergence Sublayer**
  - Service dependent functions
    - i.e. Message identification, Clock recovery
  - Adds special information to original information
    - e.g. initial MAC PDU (AAL 3/4), Frame Relay header
  - Further subdivided into CPCS and SSCS
  - Common Part Convergence Sublayer
  - Service Specific Convergence Sublayer



Types of AAL

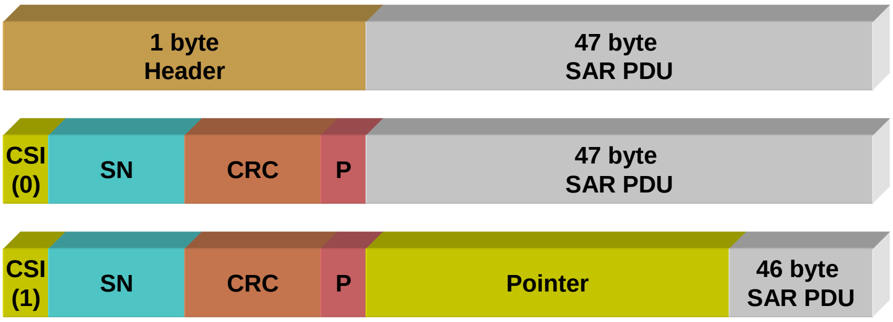
- **Service types and corresponding AAL's were defined for different traffic classes**
  - Class A (CBR) e.g. Circuit Emulation of E1, T1 frame structures
  - Class B (VBR) e.g. Packet Video, Packet Audio
  - Class C (CO-D) e.g. Frame Relay, X.25
  - Class D (CL-D) e.g. IP, SMDS

	Class A	Class B	Class C	Class D
Synchronization between Source and Destination	required	required	not required	not required
Bit rate	constant	variable	variable	variable
Connection Type	conn. oriented	conn. oriented	conn. oriented	conn. less
Adaptation Layer	AAL 1	AAL 2	AAL 3 or AAL5	AAL 4 or AAL5

AAL1

- **Purpose**
  - transfer service data units received from a source at constant rate and then deliver them at the same rate to the destination
  - optionally transfer timing information between source and destination (SRTS … Synchronous Residual Time Stamp)
  - optionally transfer TDM structure information between source and destination (e.g. timeslot 0 of E1)
- **That is Circuit Emulation Service (CES)**
- **Constant Bit Rate (CBR) service**
  - Expensive
    - Over provisioning like leased line necessary
    - Queuing prefers AAL1 cells over all other traffic (in case of congestion)

AAL1



CSI …. Convergence Sublayer Indication (1 bit) – "1" if pointer exists
SN ….. Sequence Number (3 bits)
CRC ... Cyclic Redundancy Check (3 bits)
P ……. Parity (1 bit)

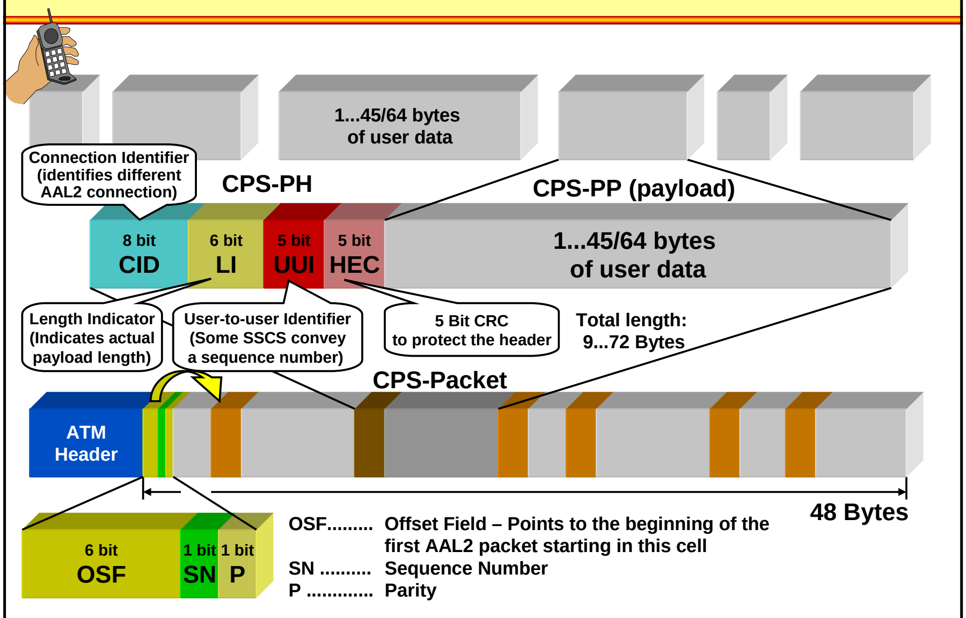
AAL2

- **Analog applications that require timing information but not CBR**
  – Variable Bit Rate (VBR)
  – Compressed audio and video
- **Relatively new (1997/98)**
  – Original standard withdrawn and later reinvented for mobile systems
- **Variable Bit Rate (VBR) service**

AAL2 for Mobile Systems

- **Cellular communication issues**
  - Packetization delay (→ QoS)
  - Bandwidth efficiency (→ Money)
- **Before AAL2 low-bit rate real-time applications were used by "partial filling" of ATM cells**
  - Using "AAL0" or AAL1
  - Very inefficient (few bytes per cell only)
- **AAL2 is designed to be fast and efficient**

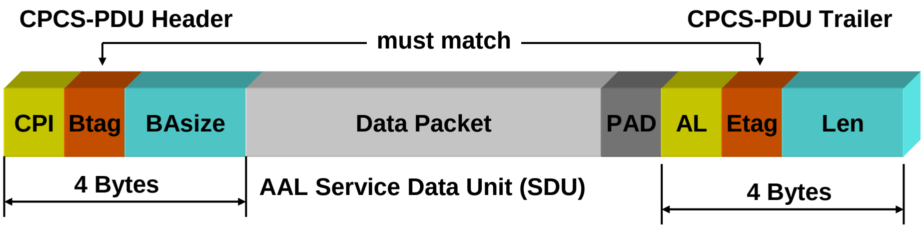
AAL2 – CS



AAL3 + AAL4

- **AAL3 designed to carry connection-oriented packets**
  - Such as X.25 or Frame Relay
- **AAL4 designed to carry connection-less datagram's**
  - Such as IP or IPX
- **Because of similarity both adaptation layers were combined to AAL3/4**

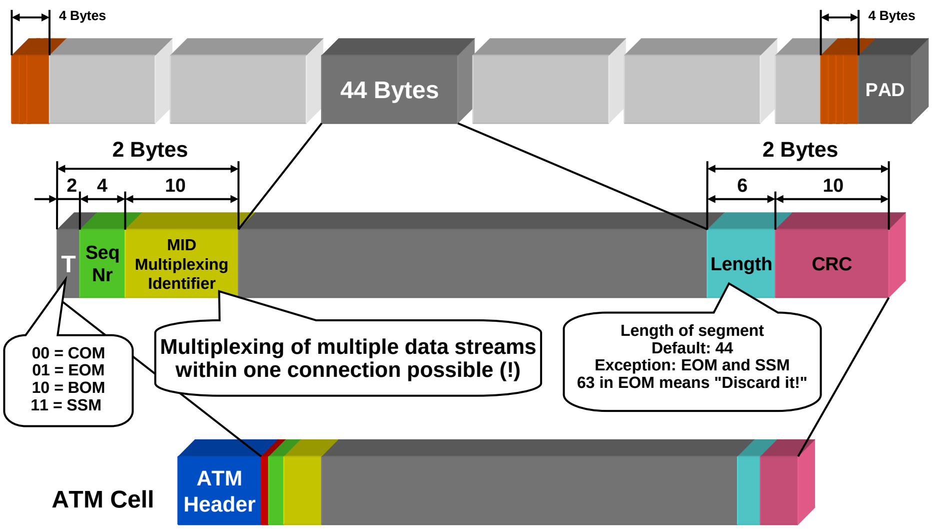
AAL3/4 – Step 1: CS



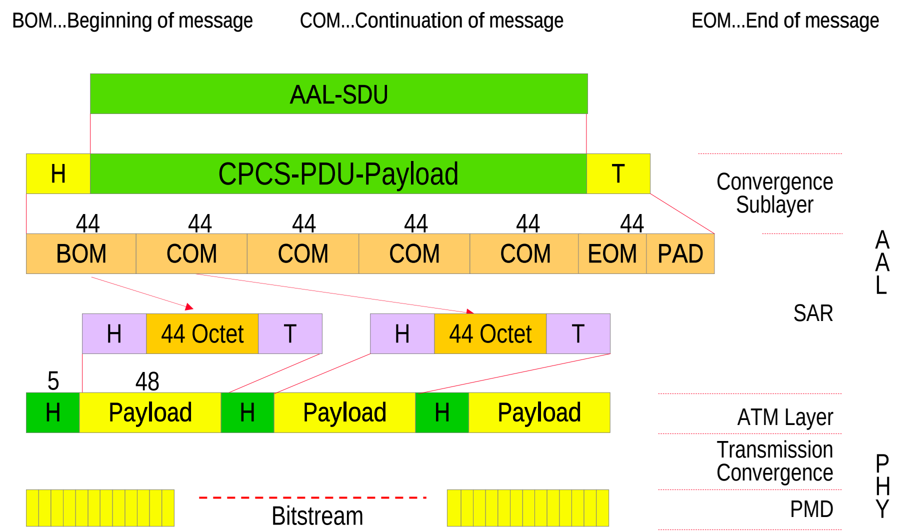
CPI .... Common Part Indicator (1Byte)
Btag....... Beginning tag (1 Byte)
BAsize... Buffer allocation size (2 Bytes)

PAD........ for 32 bit alignment
AL........... Alignment (1 Byte)
Etag........ Ending tag (1 Byte) – must match Btag
Len .... Length of SAR PDU

AAL3/4 – Step 2: SAR



AAL 3/4 - The Big Picture



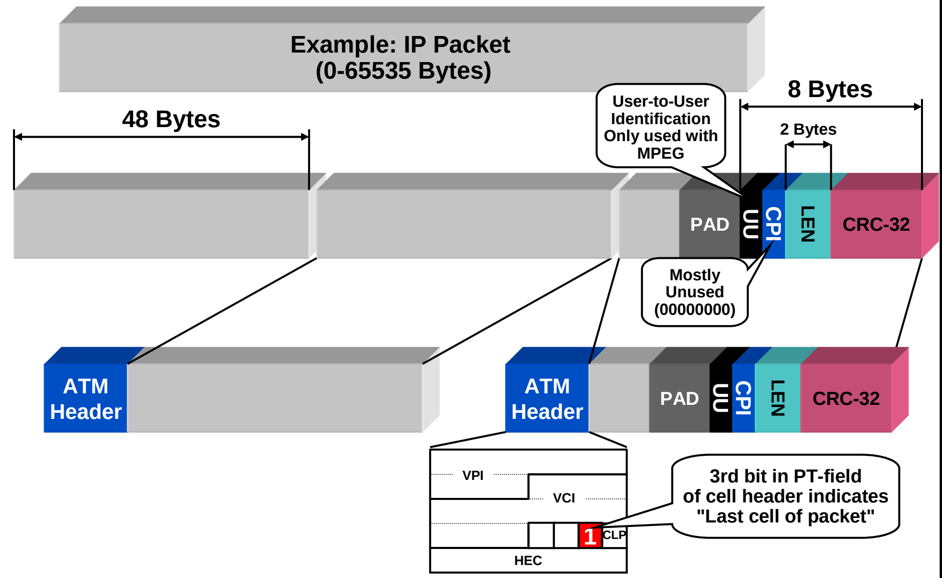
AAL3/4

- **Can multiplex different streams of data on the same ATM connection**
  - Up to 210 streams using the same VPI/VCI
- **But too much overhead**
  - Sequence numbers unnecessary when not interleaving
  - One CRC for whole packet would be sufficient
  - Length unnecessary
  - Nearly totally replaced by AAL5

AAL5

- **Favorite for data communication**
  - AAL 5 simulates connectionless data interface
  - Packet AAL with less overhead than AAL 3/4
  - Minimizes computer costs in terms of handling cells
  - Behaves as far as possible like existing data communications interfaces
  - Allows simple migration to ATM
- **Smallest overhead**
  - Convergence Layer:
    8 byte trailer in last cell
  - SAR Layer:
    just marks EOM in ATM header (PT)

AAL5 Segmentation



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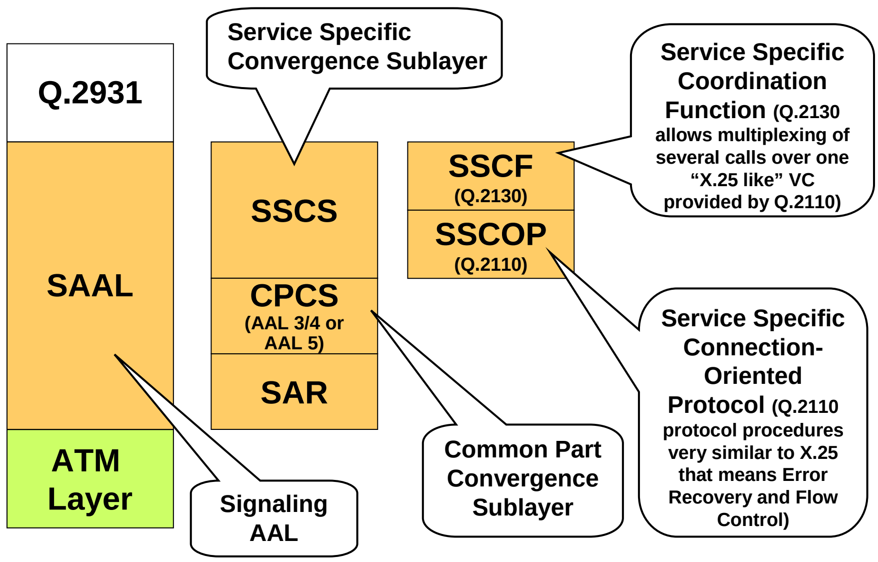
Signaling

- **ATM is connection oriented**
- **Requires signaling to establish connections**
  - between ATM-DTE and ATM-DCE (UNI)
  - between ATM-DCE and ATM-DCE (NNI)
  - Special VPI/VCI values are used for that
- **ATM Forum UNI signaling specification**
  - UNI 3.0, 3.1 and 4.0 standardized
    - UNI 2.0 PVC
    - UNI 3.0 PVC+SVC, CBR+VBR+UBR
    - UNI 4.0 +ABR, QoS Negotiation
- **Based on ITU-T Q.2931 (B-ISDN)**

Signaling Layers



Signaling Aspects

- **ITU-T**
  - Recommends AAL 3/4 for CPCS
- **ATM Forum**
  - Recommends AAL 5 for the CPCS
- **Q.2931 protocol**
  - Connection establishment
  - Negotiation of performance parameters
  - Derivate of Q.931 (N-ISDN) and Q.933 (UNI signaling protocol for Frame Relay)
  - VPI/VCI used instead of a D-channel (N-ISDN)
  - Uses meta signaling to establish signaling paths and channels (ITU-T)

VPI/VCI for ATM Signaling

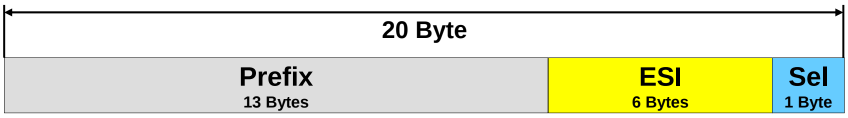
- **Reserved UNI headers - ITU-T**
  - Meta signaling - VPI=0, VCI=1
  - Broadcast signaling - VPI=0, VCI=2
- **Reserved UNI headers - ATM Forum**
  - Meta signaling - VPI=0, VCI=1
  - Broadcast signaling - VPI=0, VCI=2
  - Point-to-point signaling - VPI=0, VCI=5
  - ILMI - VPI=0, VCI=16
  - PNNI - VPI=0, VCI=18

Meta - Signaling

- **Major functions**
  - Signaling channel connection setup
  - Signaling channel connection control
  - Signaling channel connection release
- **Used for the negotiation of the required VPI/VCI combination**
  - Signaling of signaling

ATM Addresses

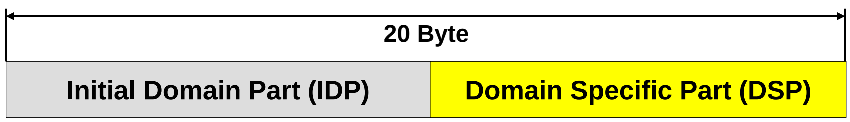
- **Different types of ATM addresses**
- **All have 20 byte length**
- **All consist of three main parts**
  - Prefix (Basically topology information)
  - End System Identifier (ESI)
  - NSAP Selector (Selects application)
- **ATM address is a structured**
  - note: structured means that it contains topology specific information



ATM Addresses

- **ATM Forum defined three formats for ATM End System Address (ASEA)**
  - DCC ASEA format
  - ICD ASEA format
  - E.164 ASEA format
- **Private networks support ISO DCC and ICD formats**
- **Only public networks may use E.164 address format**
- **All formats**
  - are based on structured ISO Network Service Access Point (NSAP) addresses

ISO NSAP



IPD ... to identifies the network addressing authority responsible for assignment and allocation of the DSP

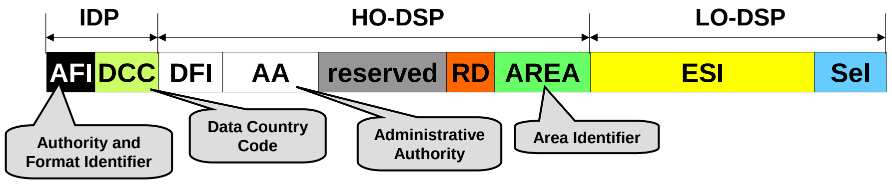
DSP ... is defined by the corresponding addressing authority and consists of

High Order DSP (HO-DSP)
for identifying networks on a prefix level

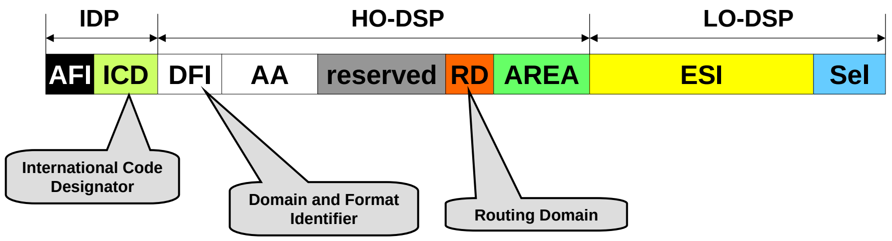
Low Order DSP (LO-DSP)
for identifying end systems

Address Flavors

DCC ATM Address Format (AFI=39)



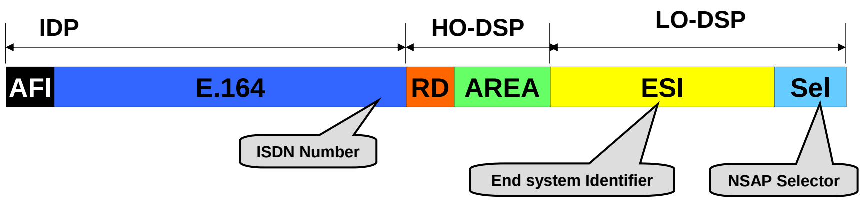
ICD ATM Address Format (AFI=47)



HO-DSP may contain other sectioning than DFI, AA, RD and AREA

Address Flavors (cont.)

E.164 ATM Address Format (AFI=45)



HO-DSP may contain other sectioning than RD and AREA