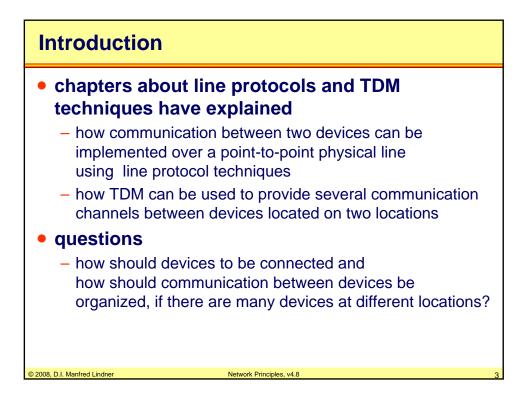
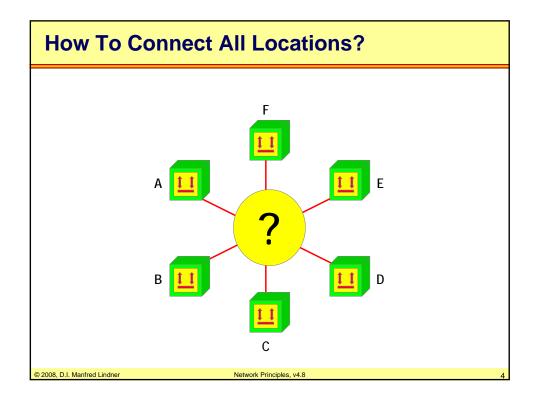
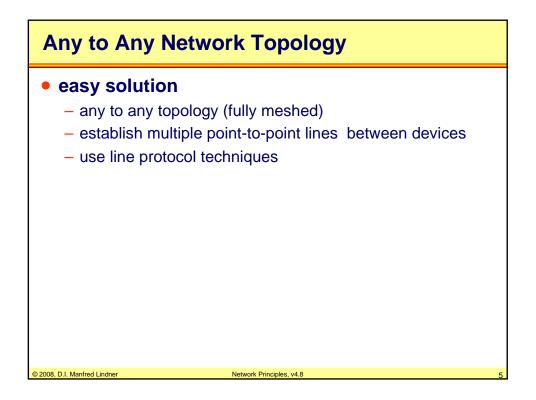
# **Network Principles**

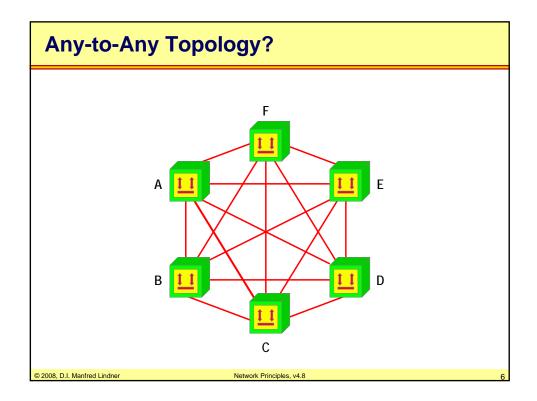
Circuit Switching, Introduction ISDN Packet Switching, Datagram versus Virtual Call OSI Model, Introduction to ISDN, X.25, FR and ATM

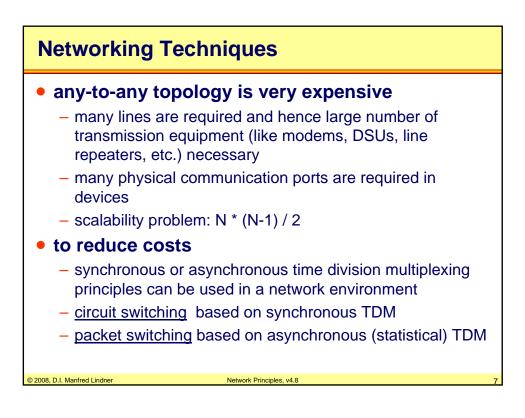
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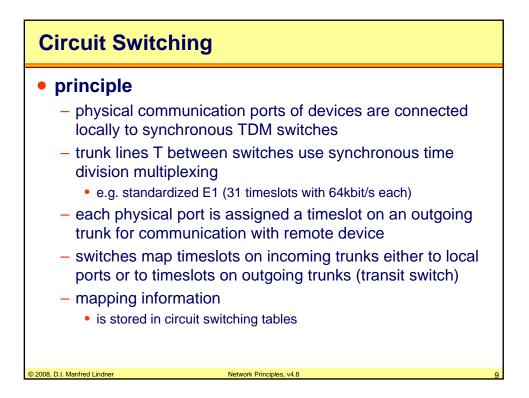


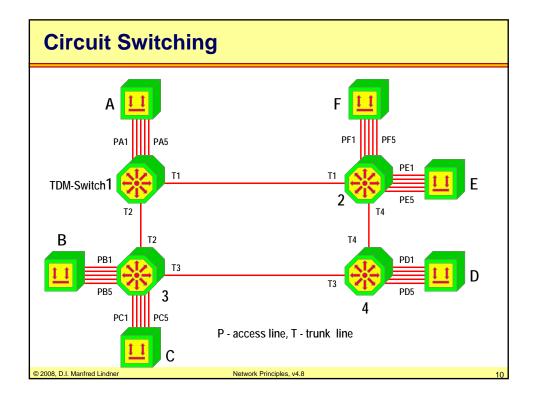


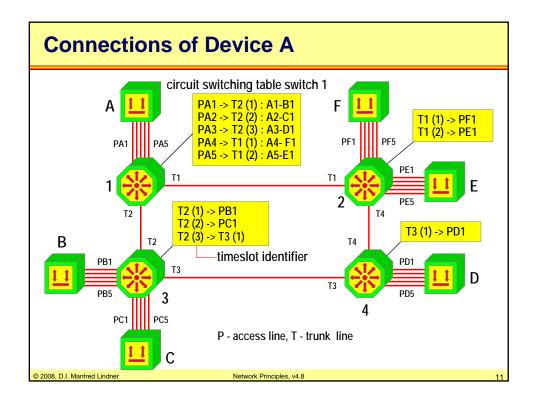


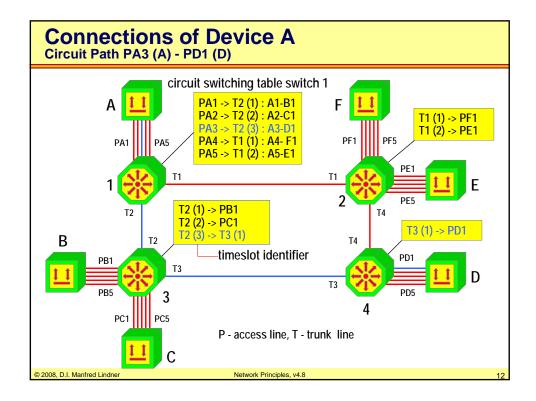


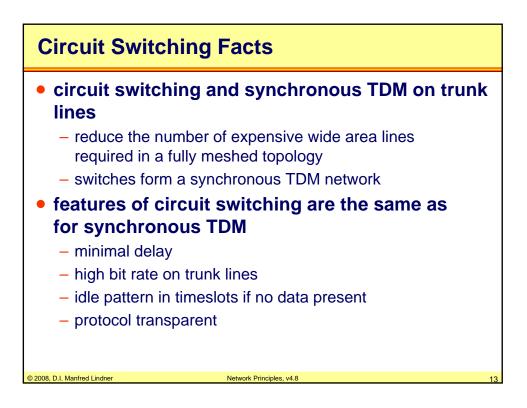
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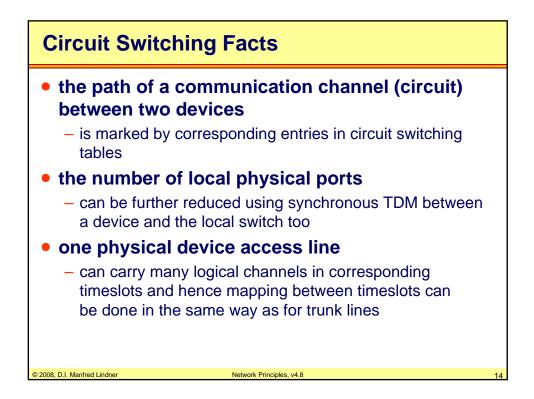


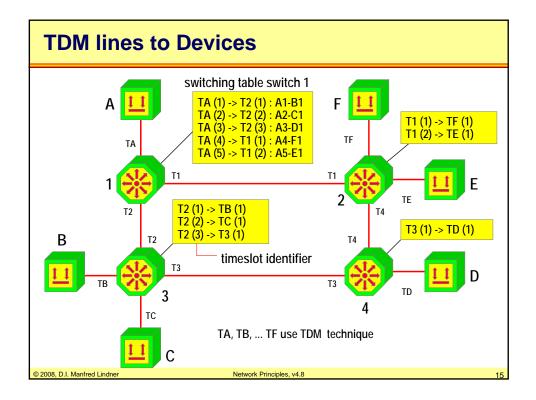


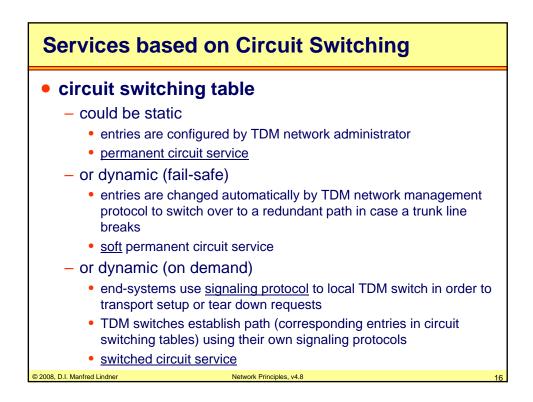


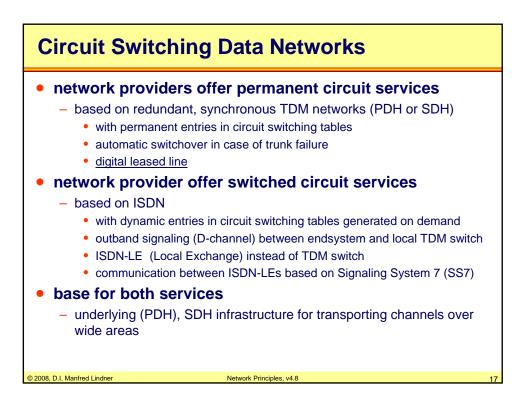


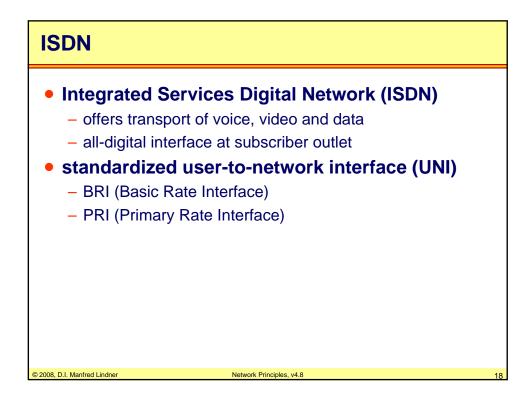


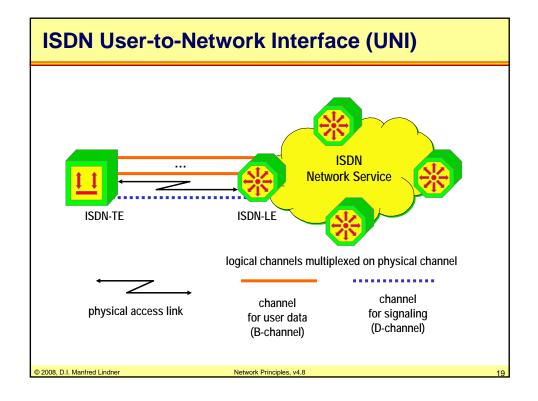


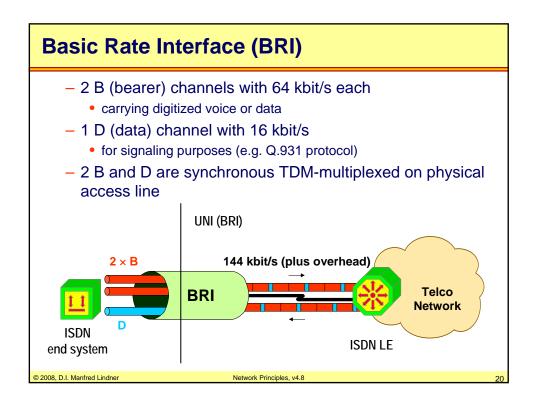


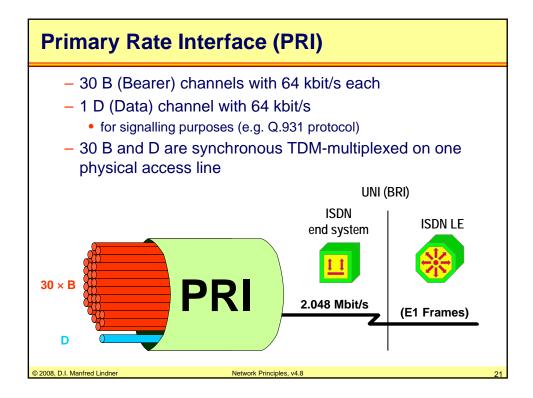


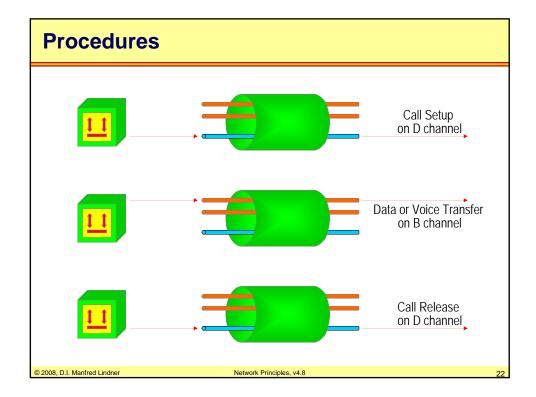


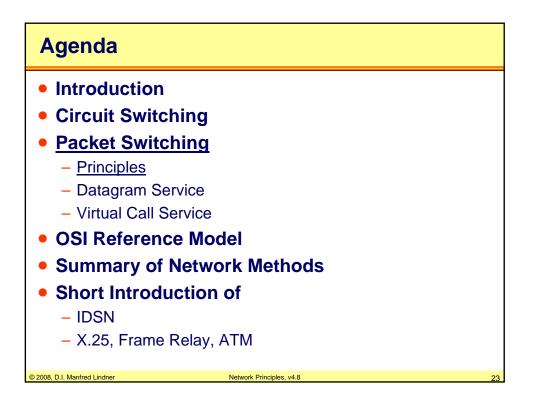


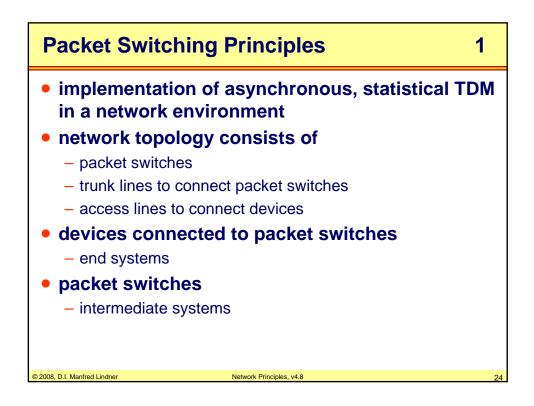


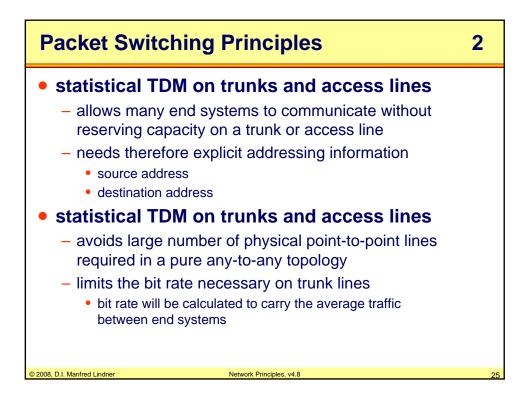


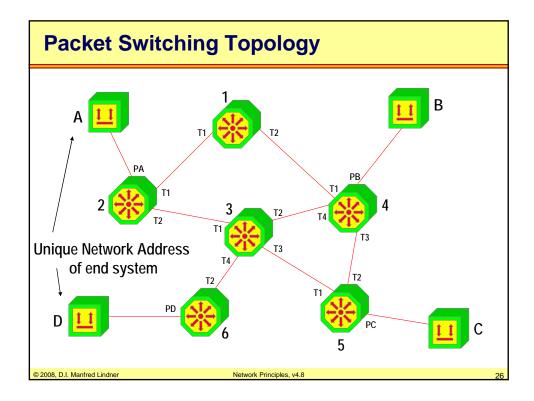


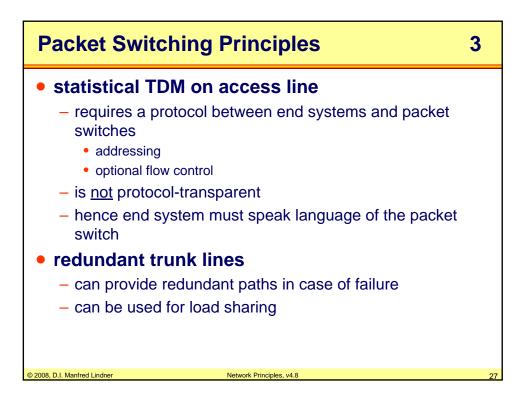


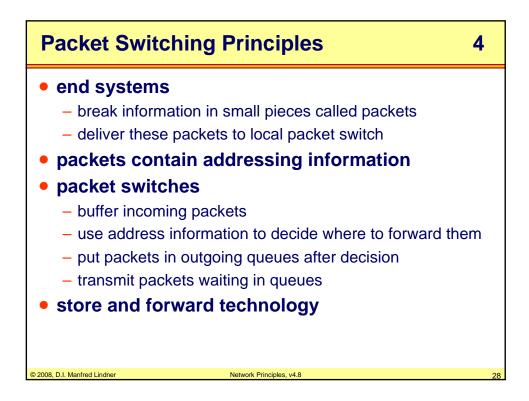


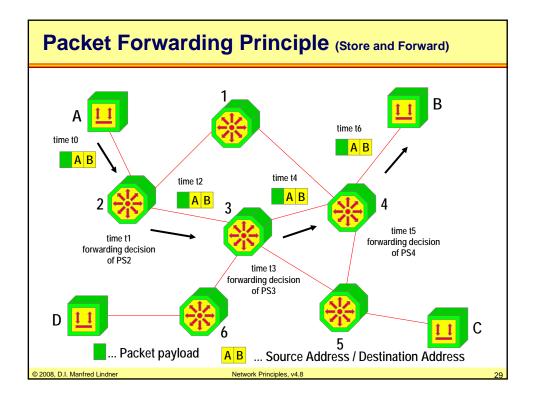


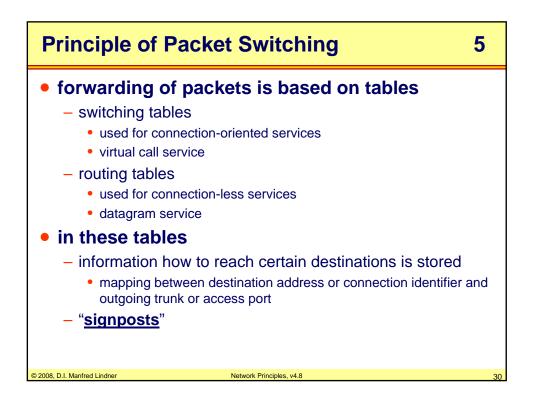




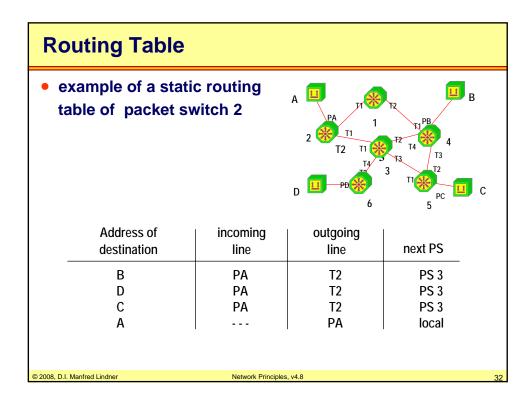


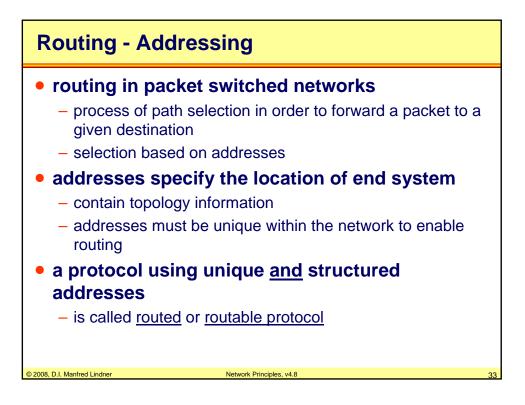


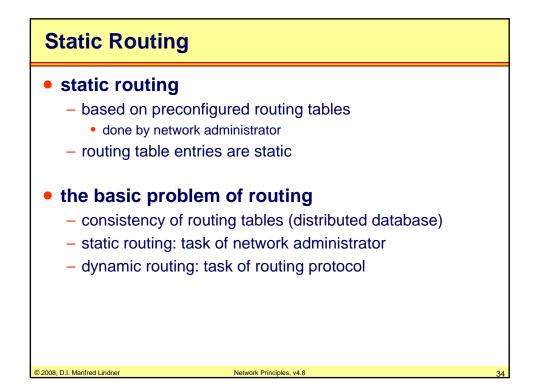


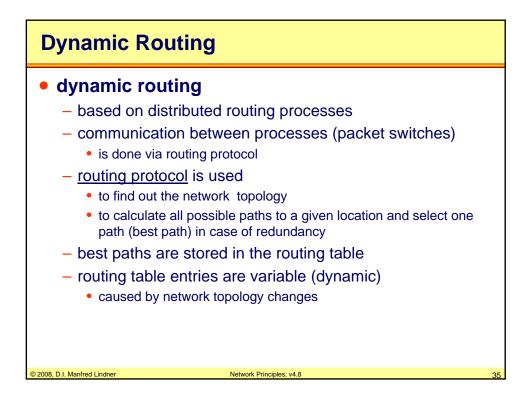


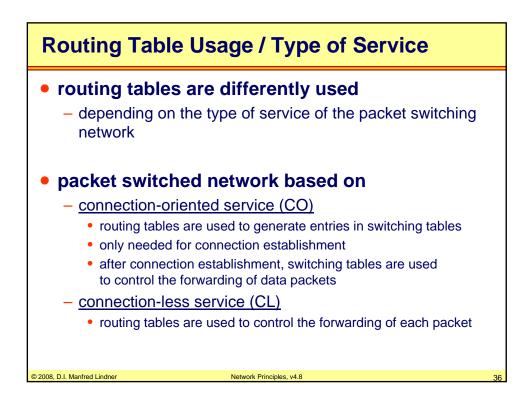
Routing Table		а 🔲 🔐	В	
• example of a static routing table of packet switch 3 D D PP C C C				
Address of	incoming	outgoing		
destination	line	line	next PS	
В	T1	T2	PS 4	
С	T1	Т3	PS 5	
D	T1	T4	PS 6	
В	T2	kill		
В	Т3	kill		
В	T4	kill		
С	T2	T3	PS 5	
С	Т3	kill		
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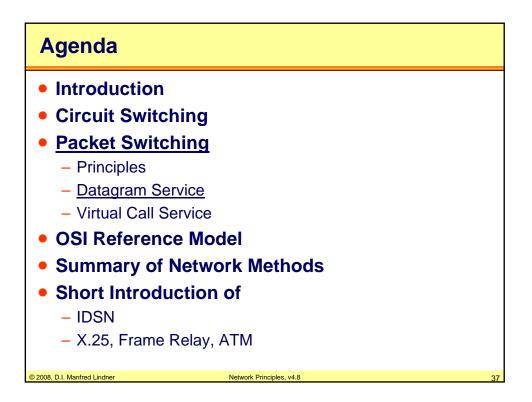


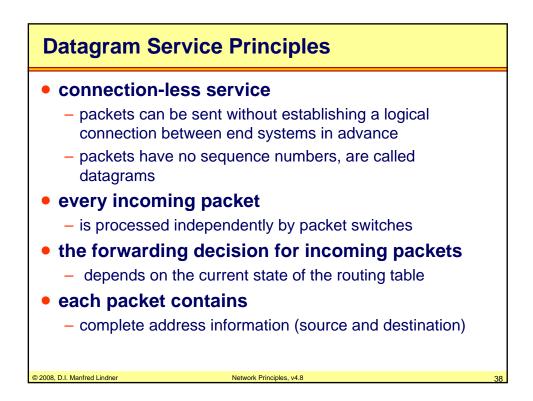


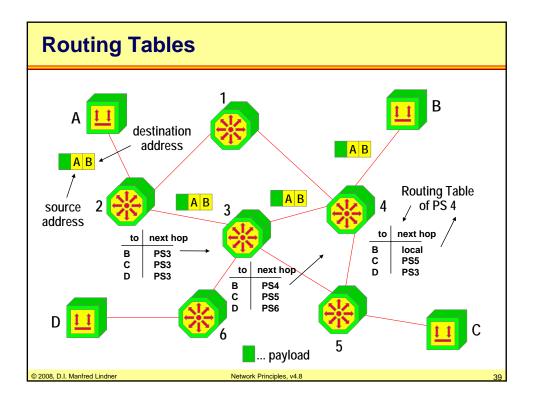


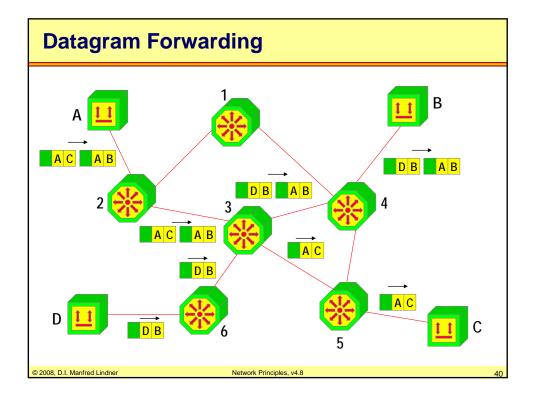


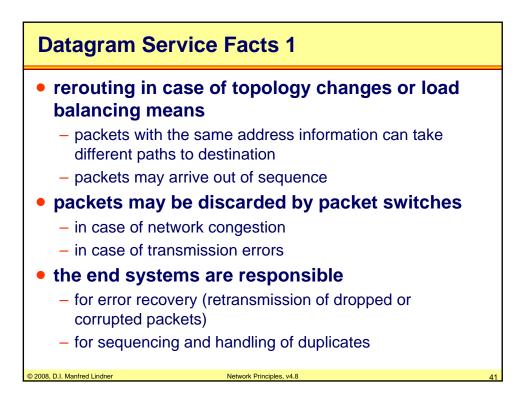


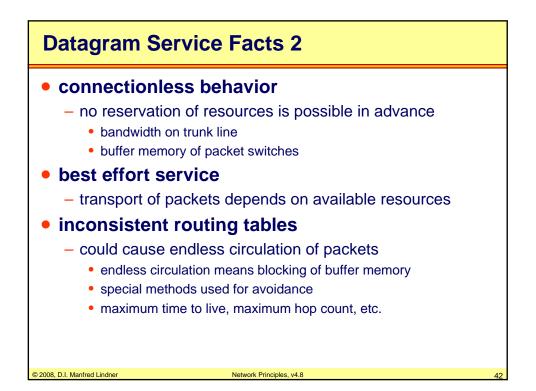


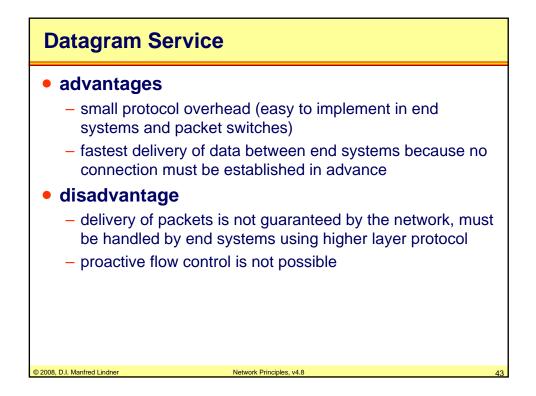


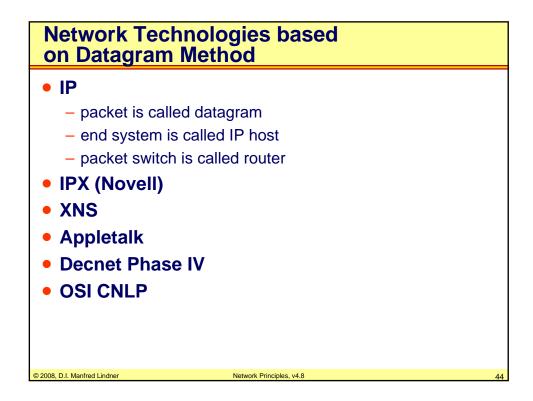


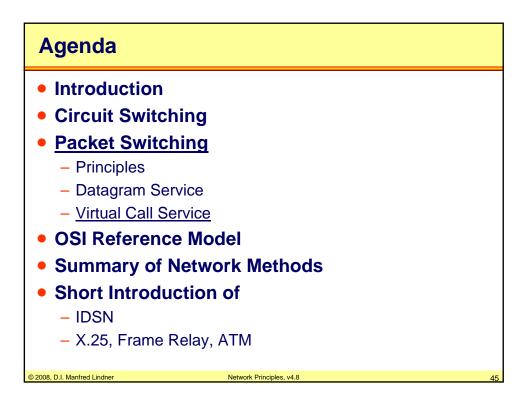




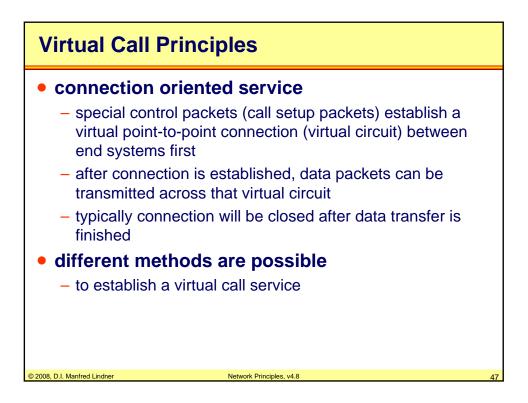


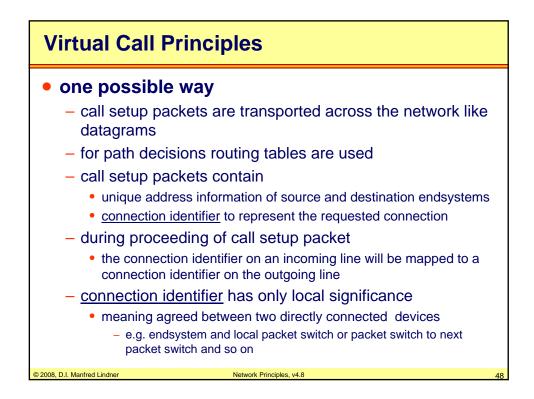


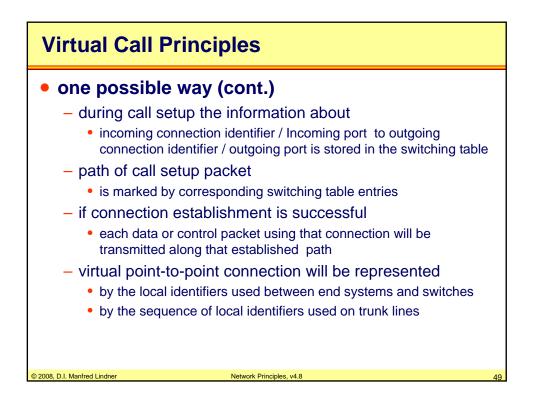


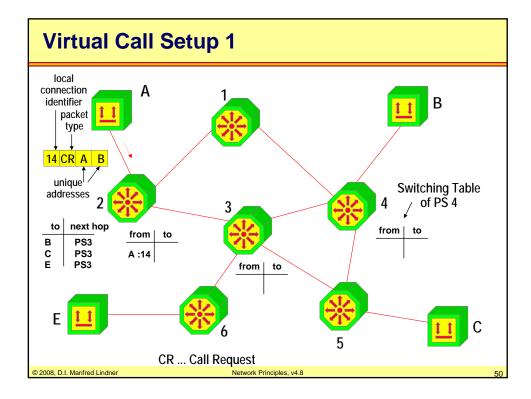


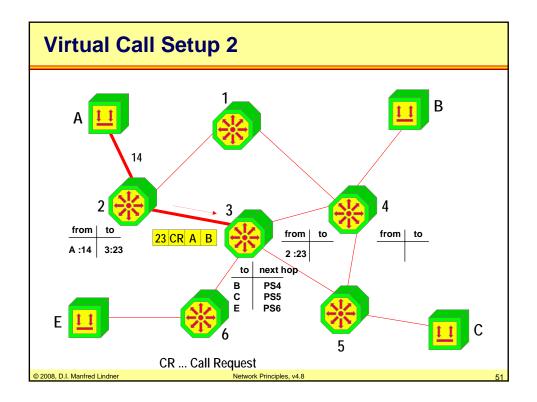
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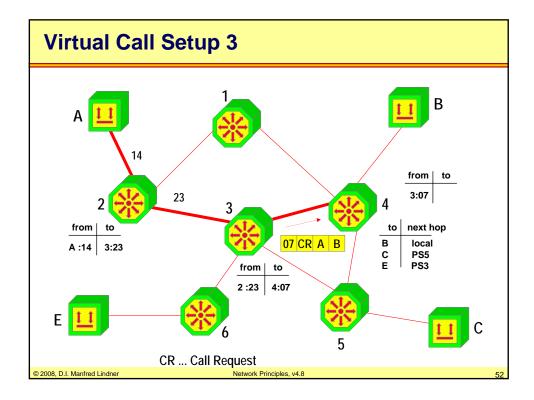


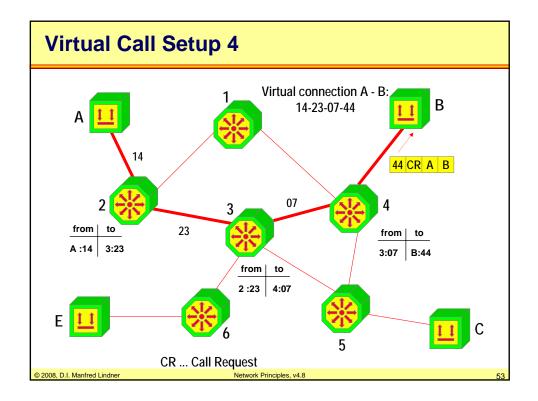


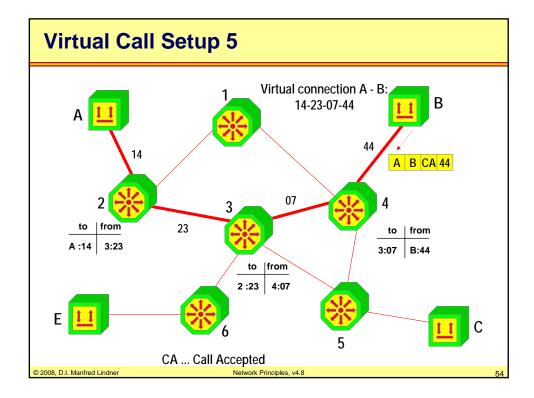


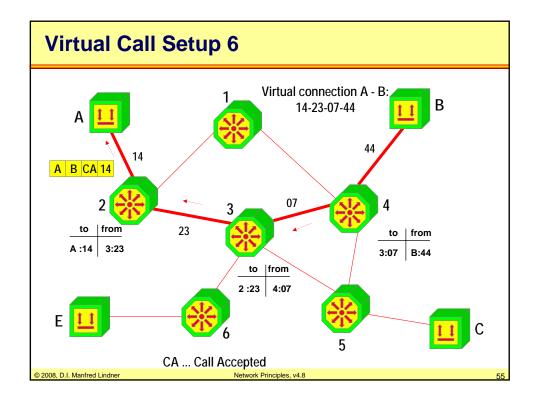


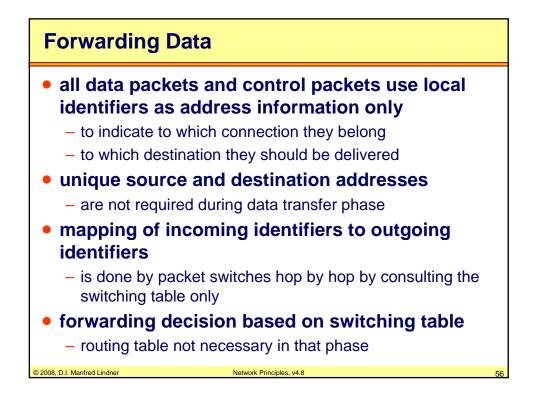


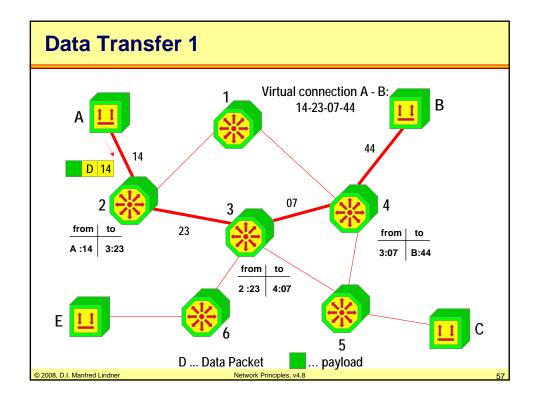


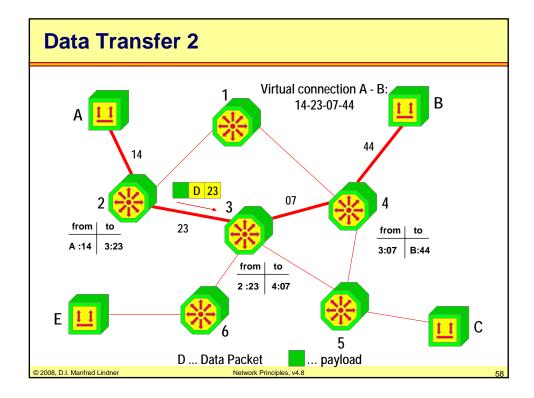


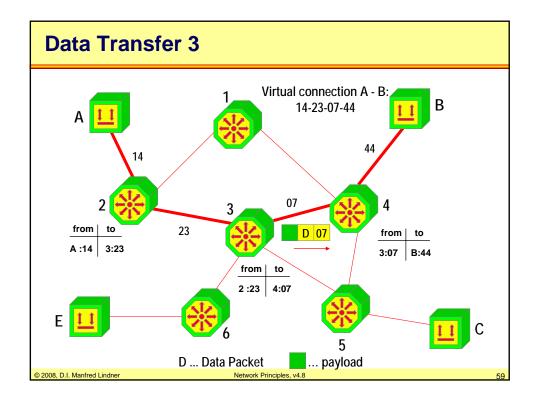


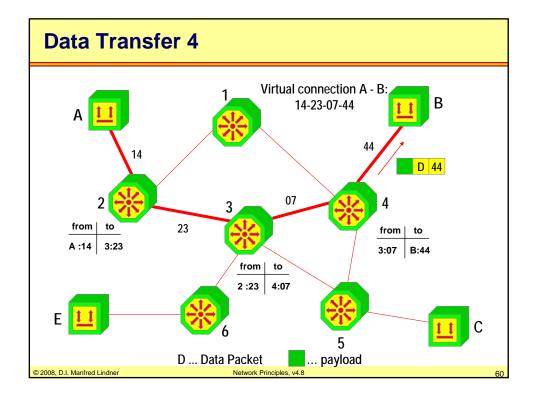


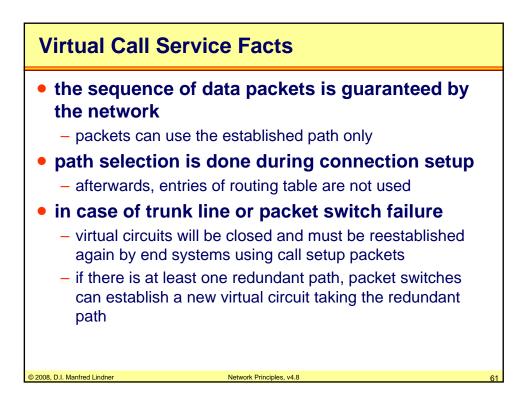


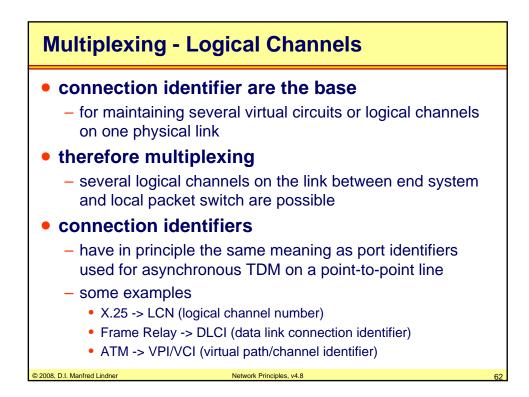


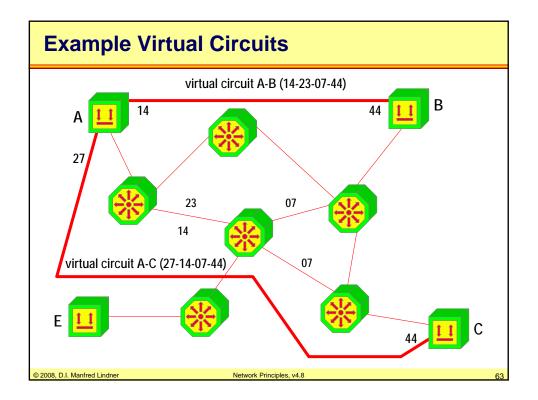


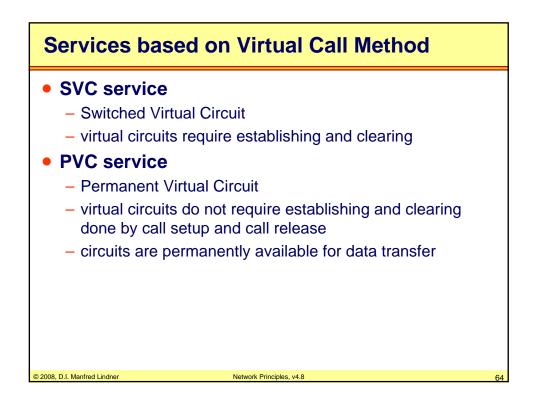


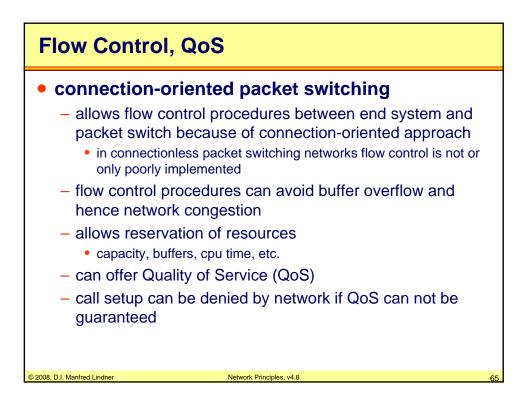


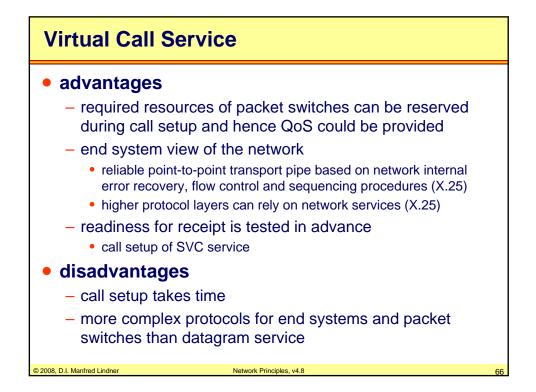


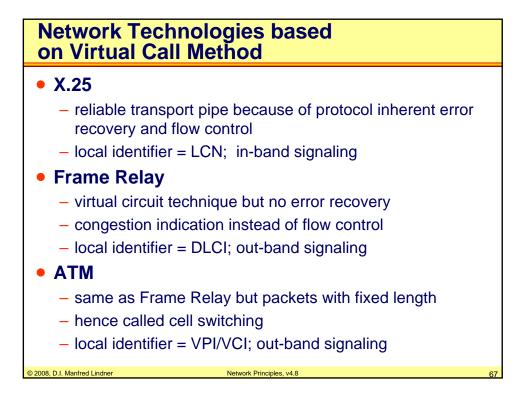




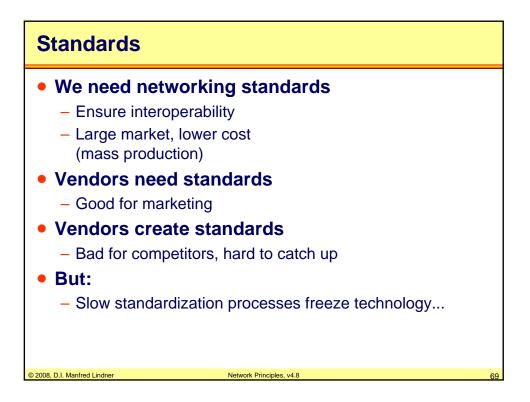




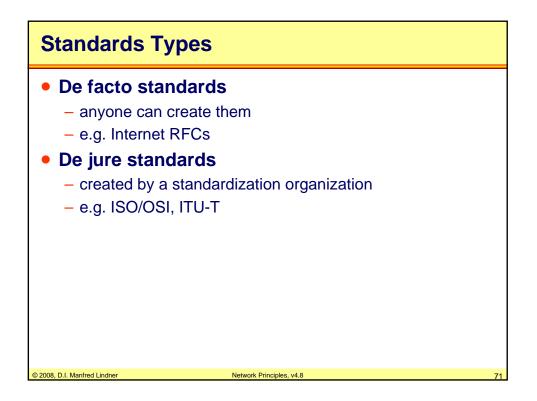


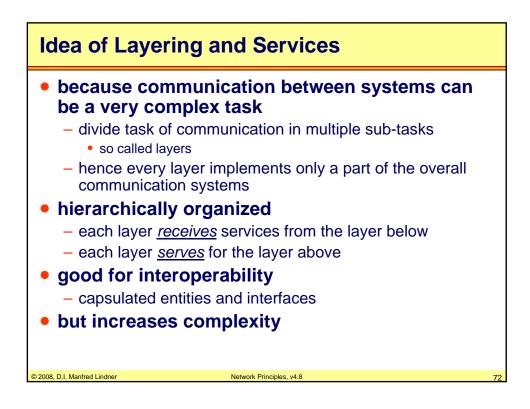


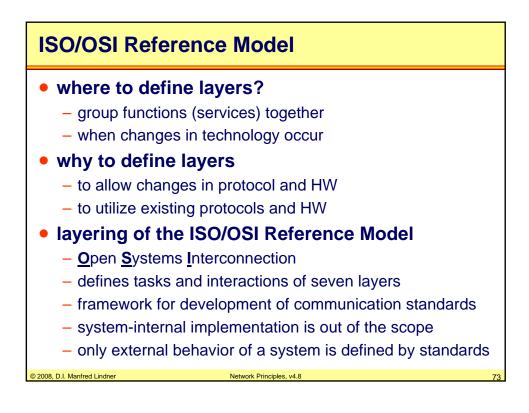
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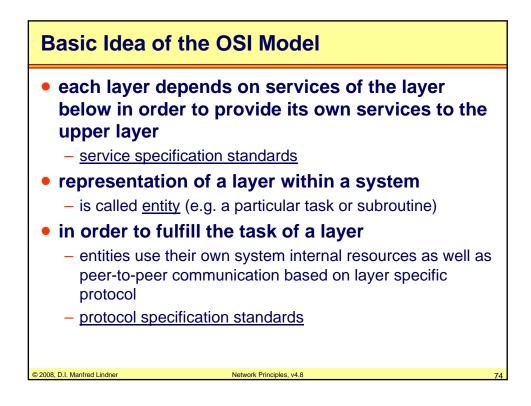


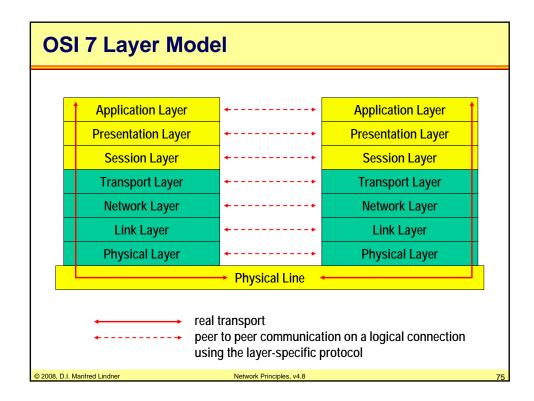


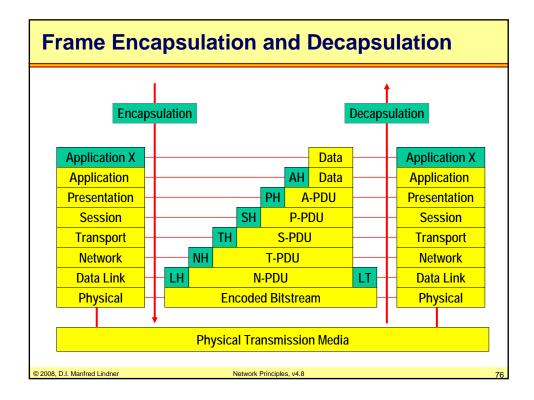


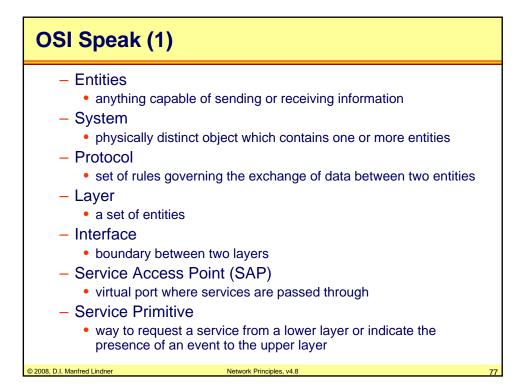


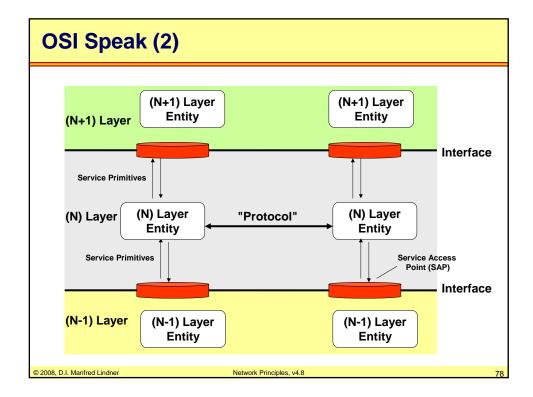


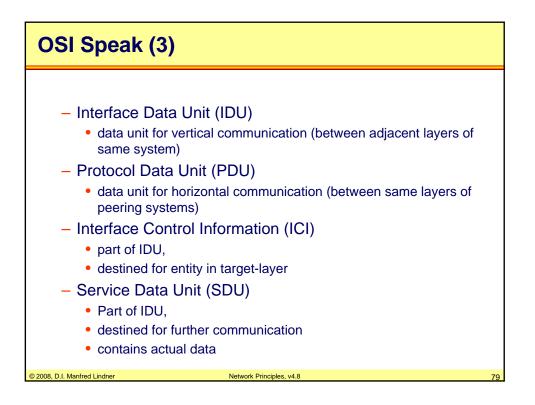


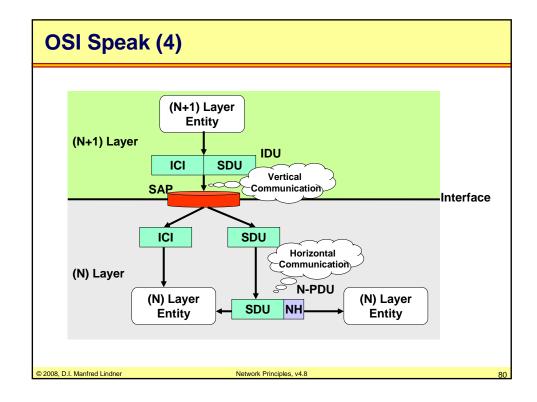


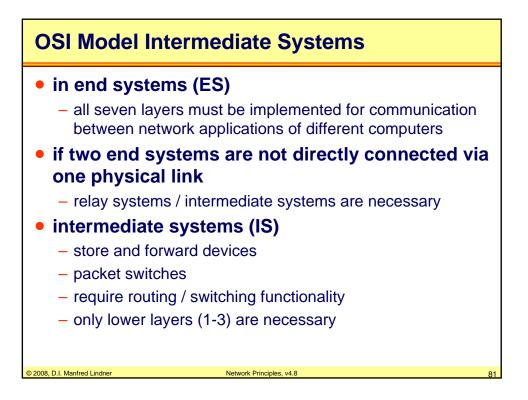


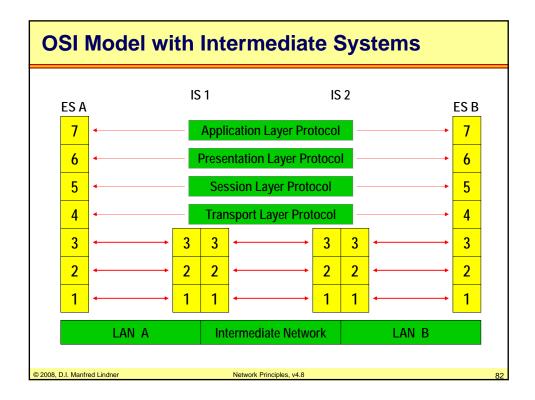


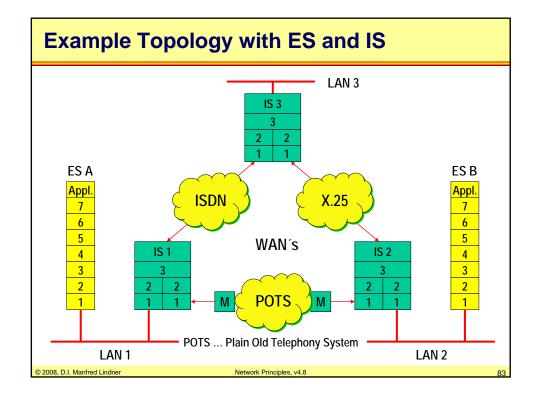


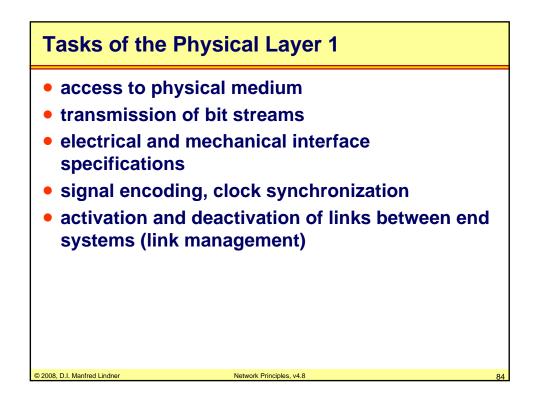


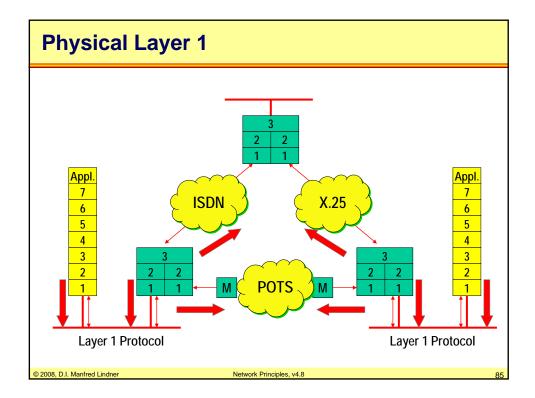


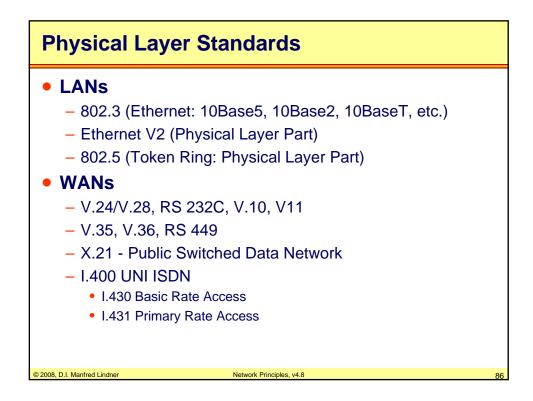


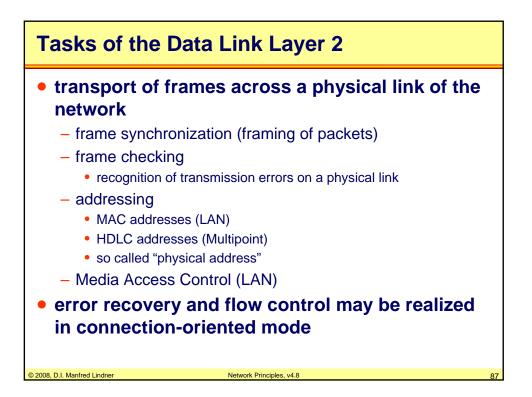


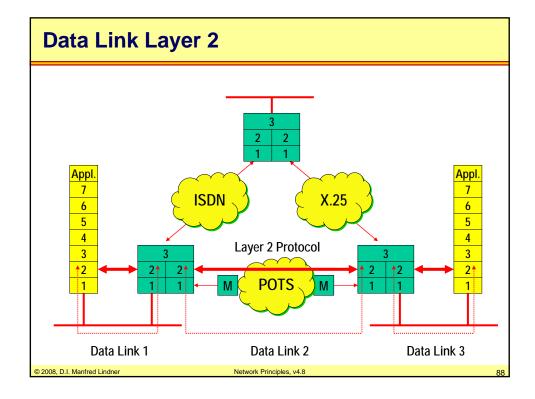


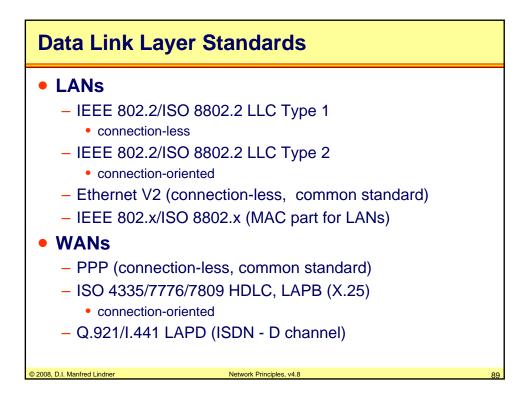


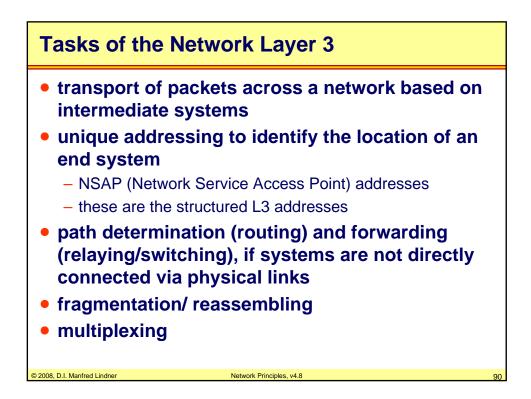


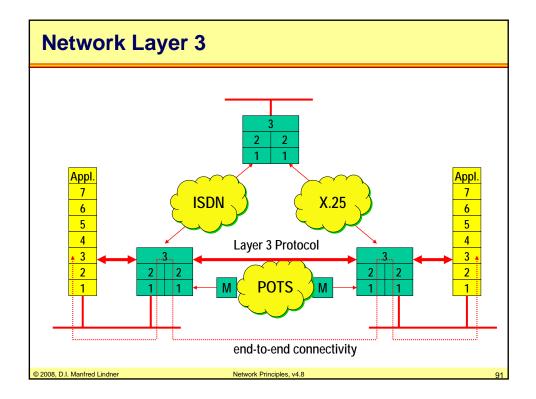


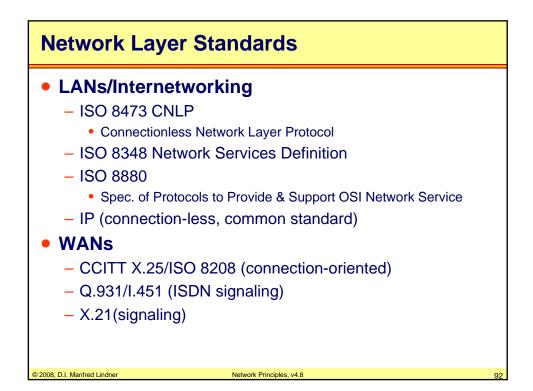


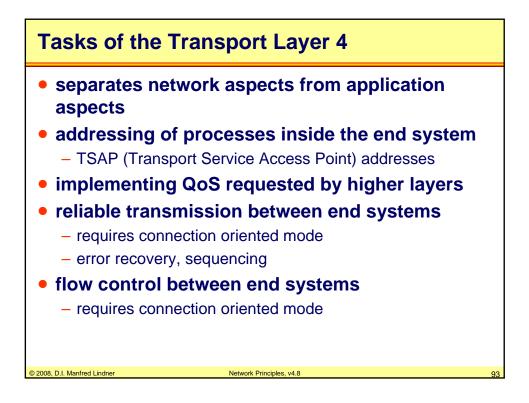


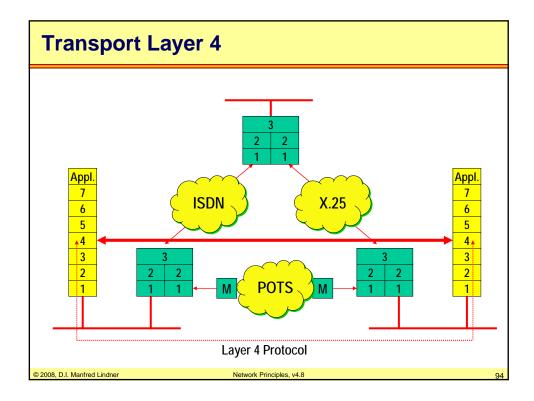


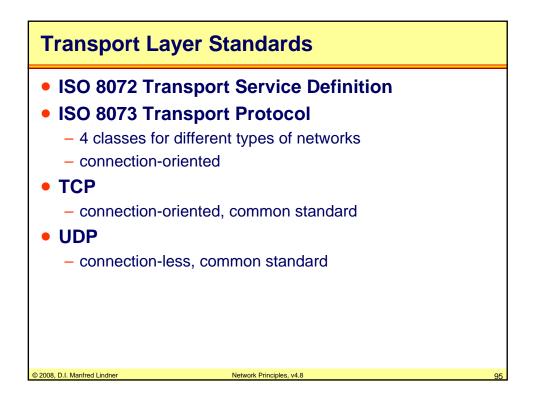


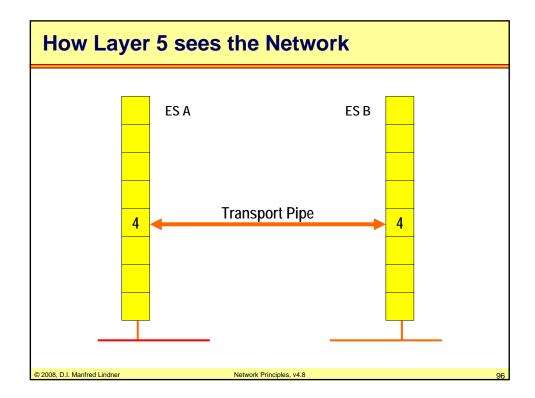


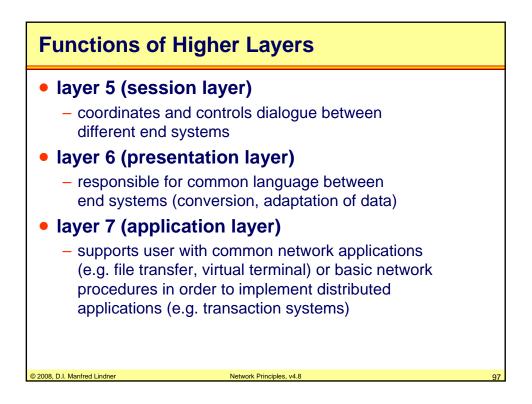


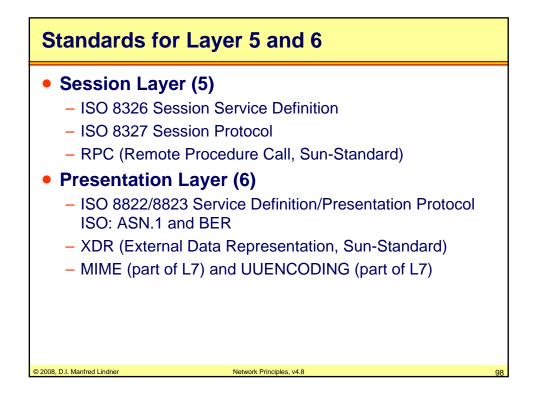


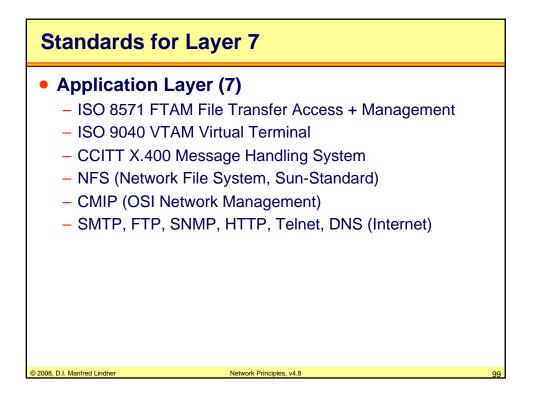


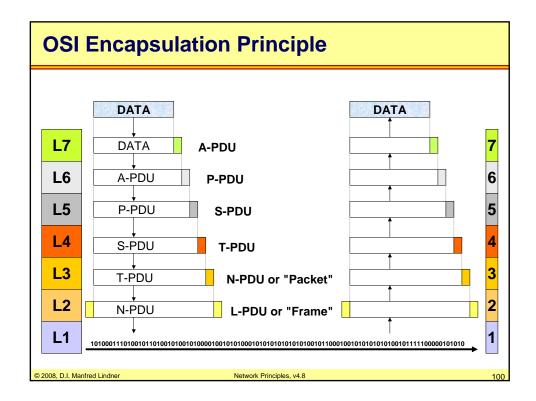


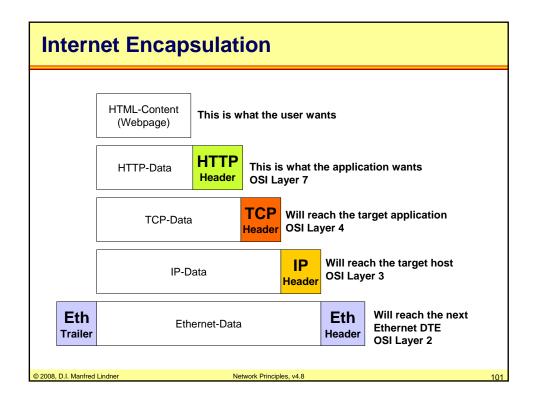


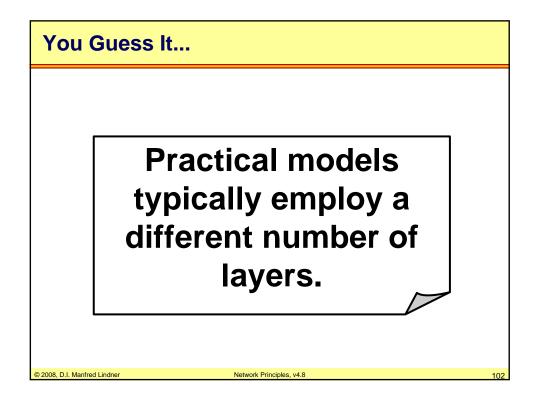


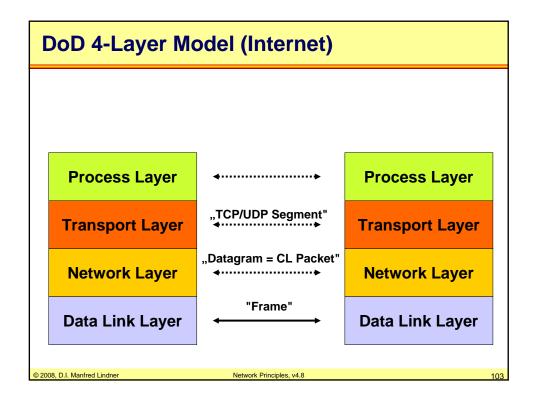


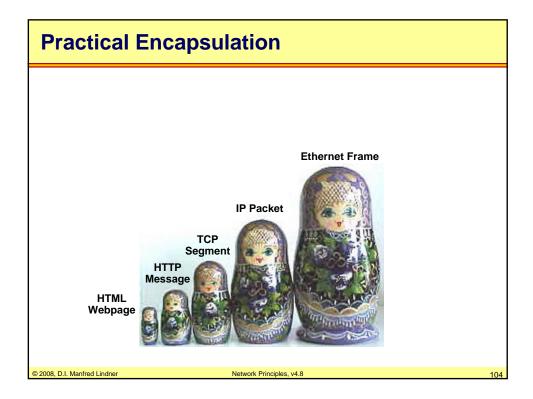


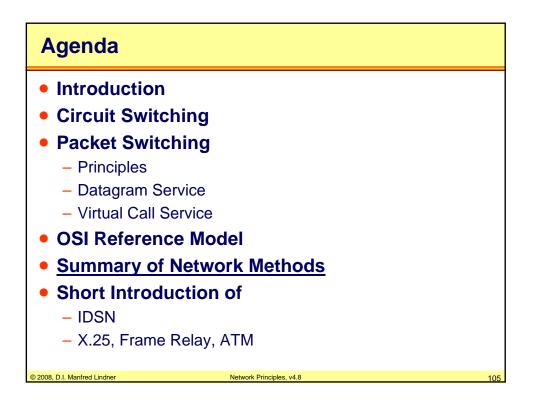


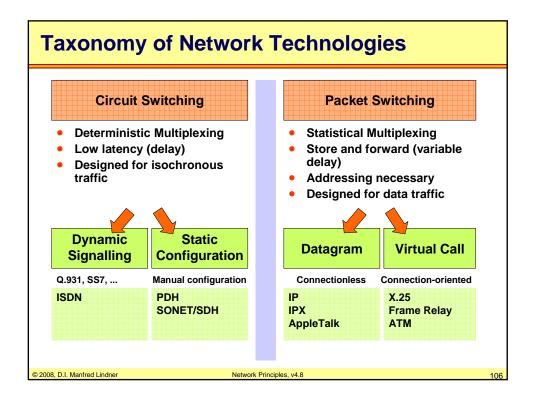


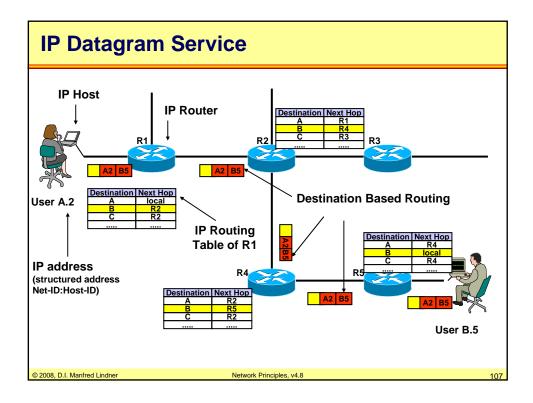


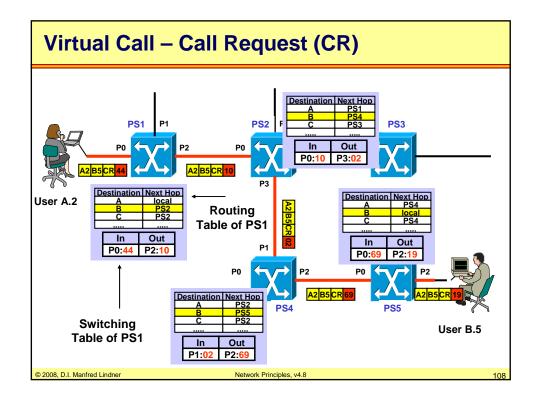


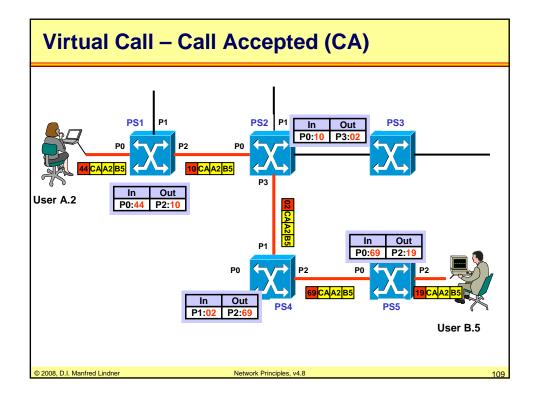


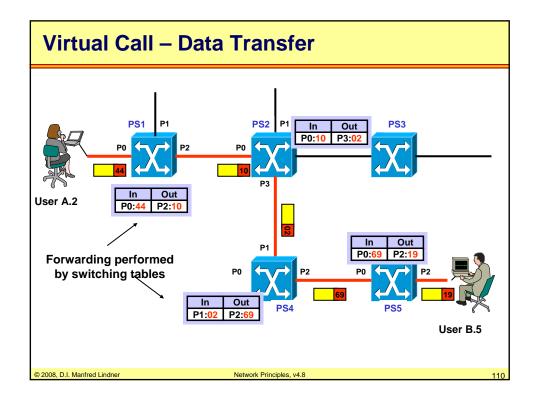


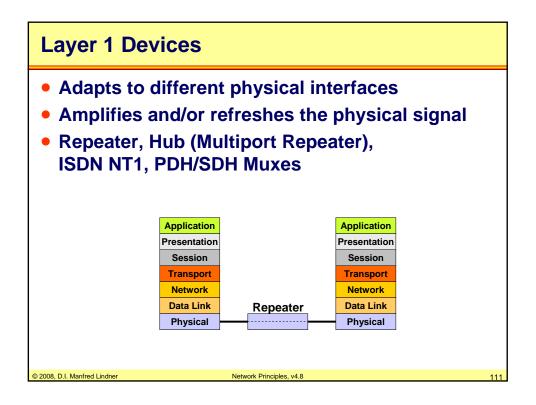


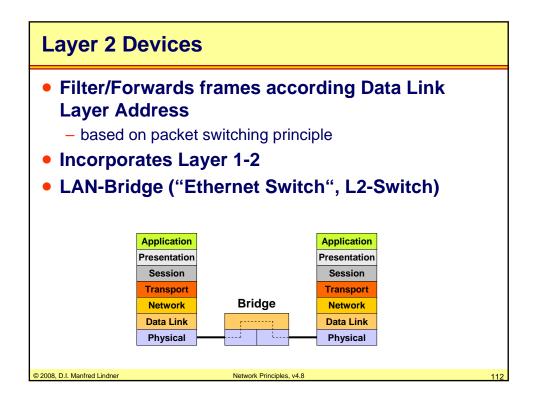


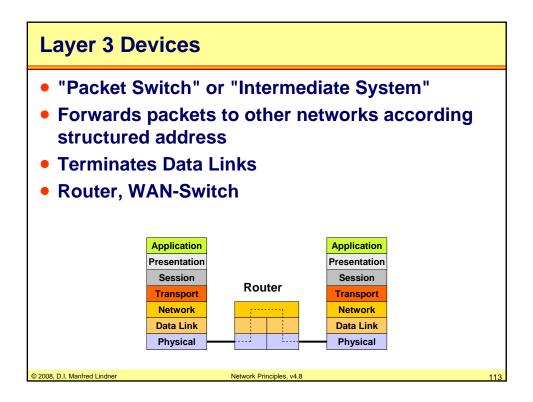


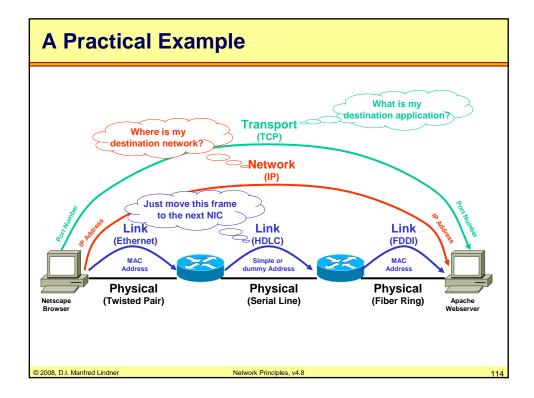


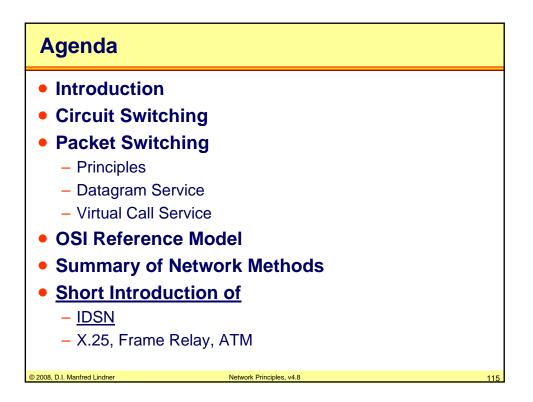


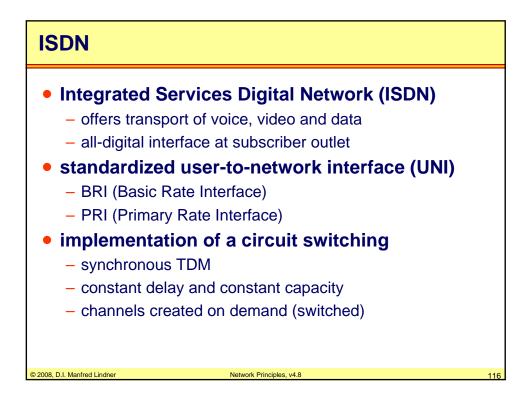


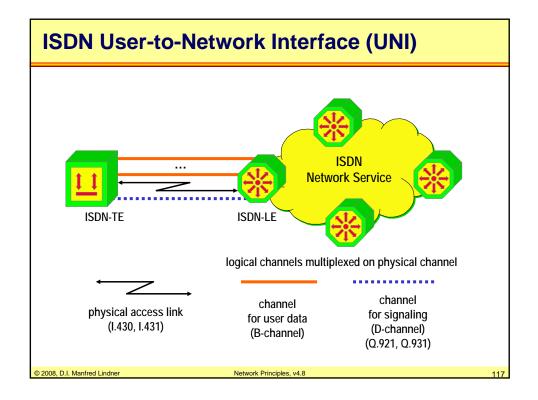


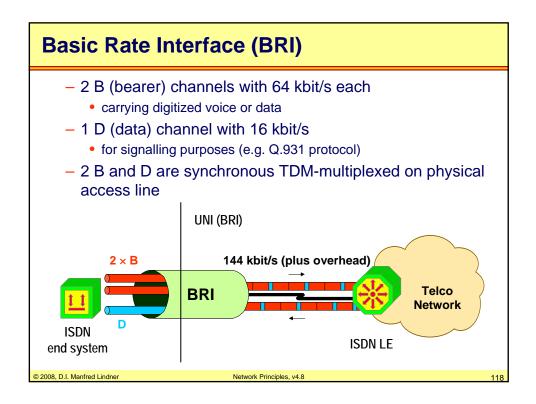


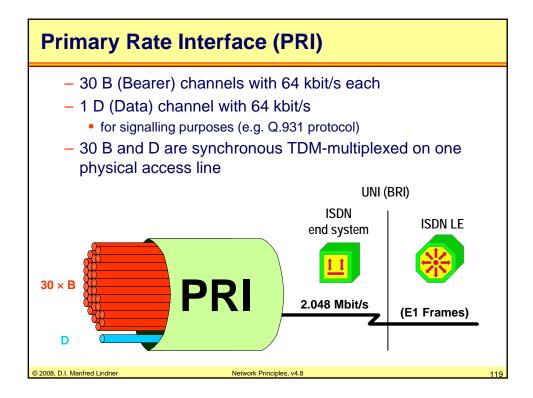


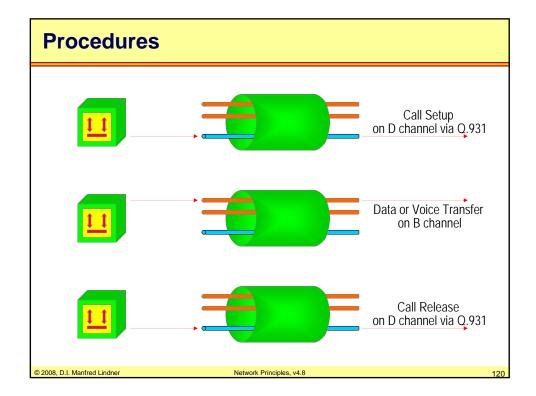


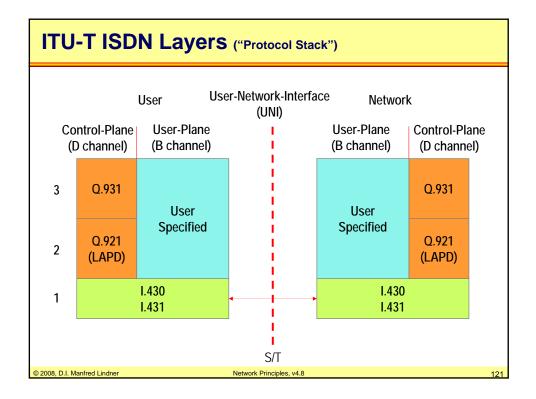




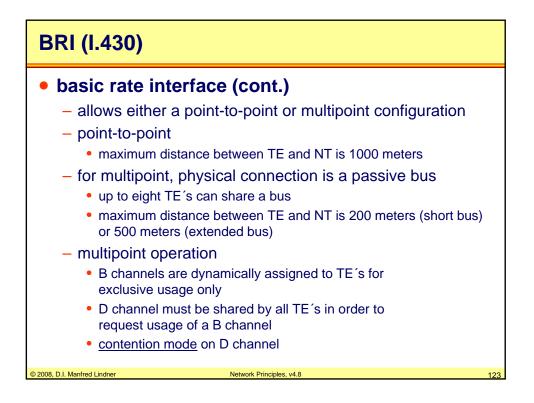


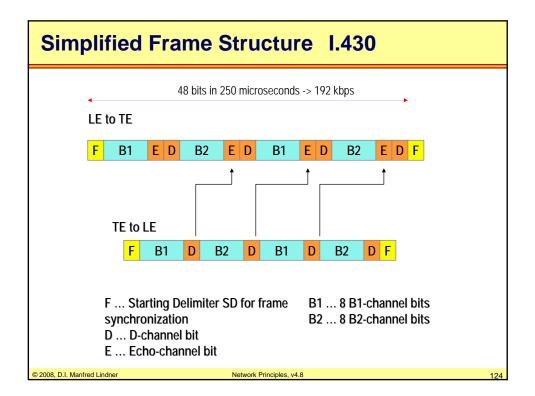


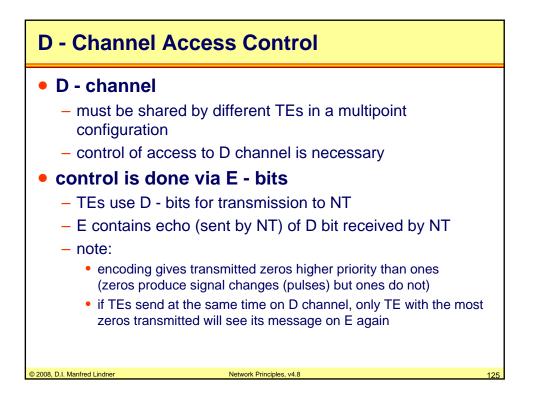


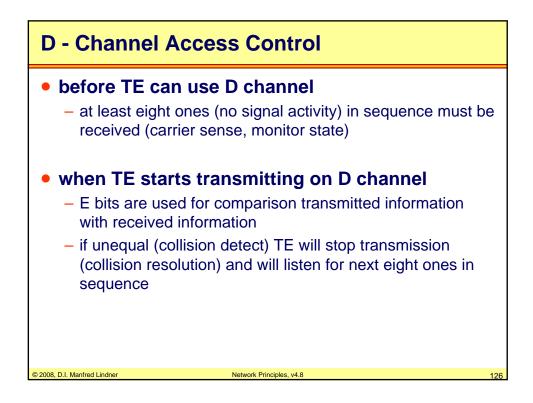


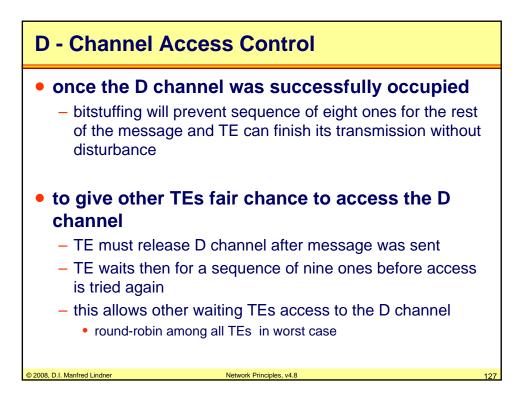
BRI (I.430)		
<ul> <li>basic rate interface (S<sub>0</sub>/T interface)</li> </ul>		
<ul> <li>four wires with terminating resistor (100 ohm)</li> </ul>		
<ul> <li>RJ45 connector with 8 leads</li> </ul>		
<ul> <li>2 transmit + 2 receive with power source 1 via phantom circuit</li> </ul>		
<ul> <li>4 optional power feeds for power source 2 and 3</li> </ul>		
<ul> <li>modified AMI code (zero causes alternate pulses)</li> </ul>		
<ul> <li>positive or negative pulse of 750mV + -10%</li> </ul>		
<ul> <li><u>bitstuffing</u> prevent long sequences of ones on D-channel</li> </ul>		
<ul> <li>frame synchronization based on code violations</li> </ul>		
<ul> <li>– frame of 48 bit is transmitted in a period of 250 usec</li> </ul>		
<ul> <li>– 192 kbps total speed</li> </ul>		
<ul> <li>2 B channels at 64 kbps, 1 D channel at 16 kbps</li> </ul>		
<ul> <li>48 kbps for framing, DC balancing and D-channel mirroring</li> </ul>		
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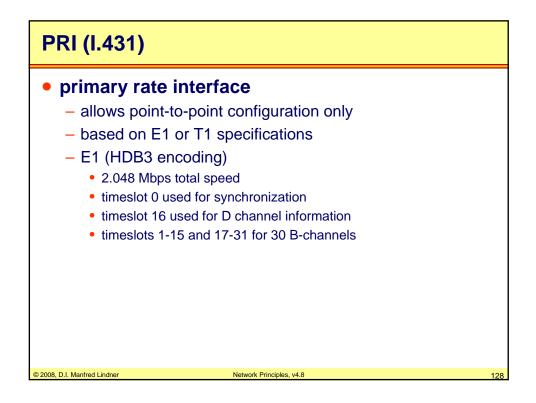


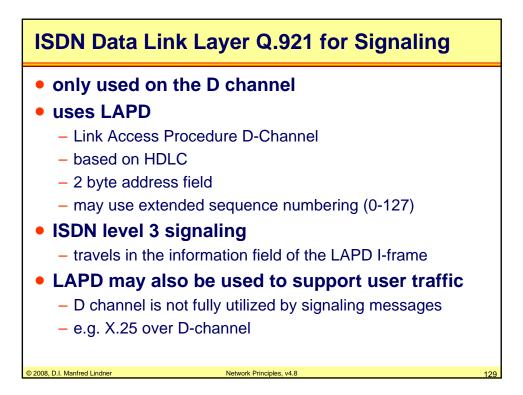


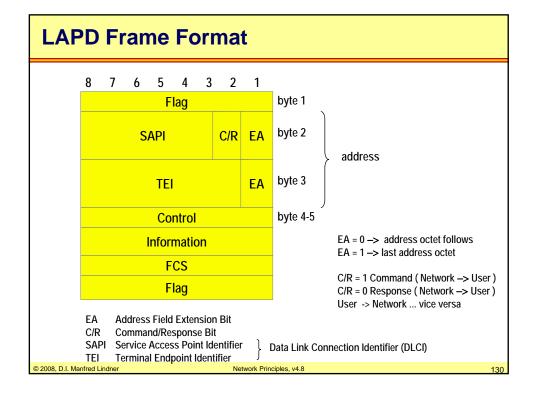


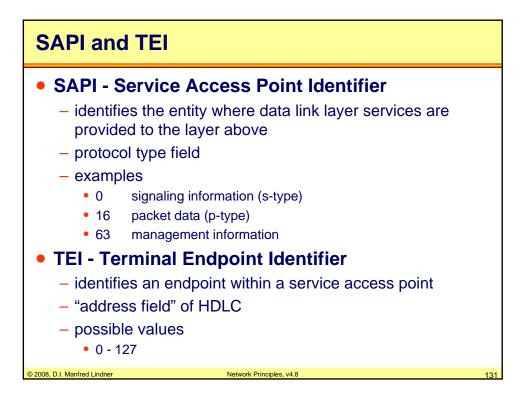


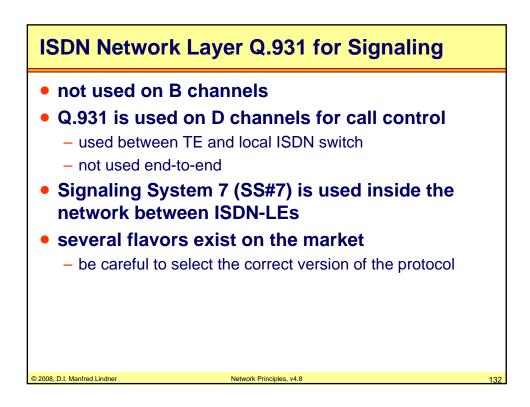


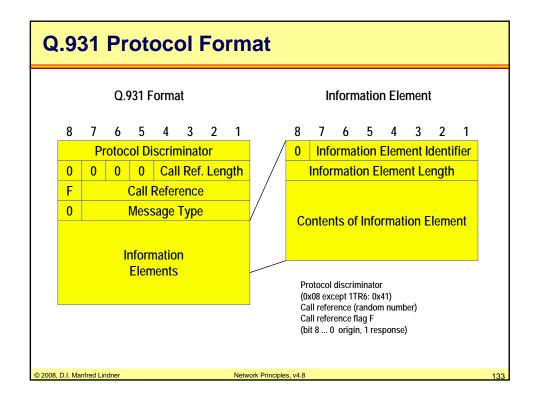




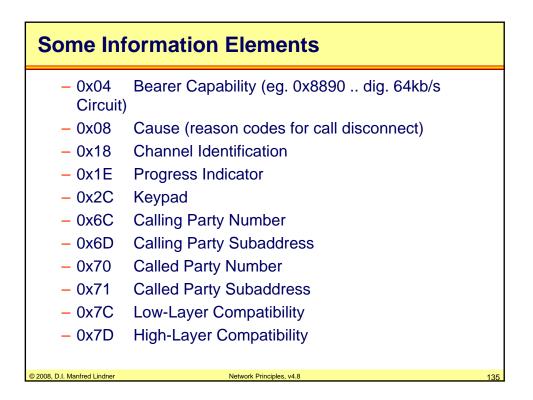


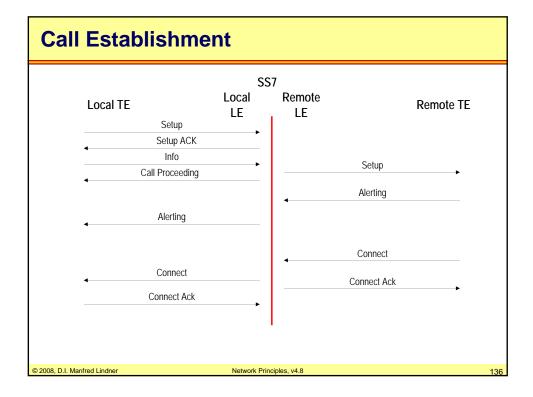


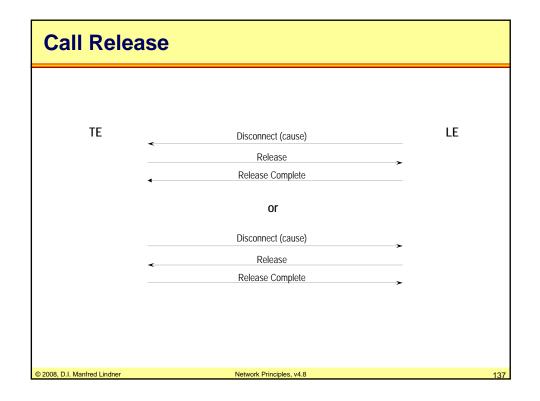


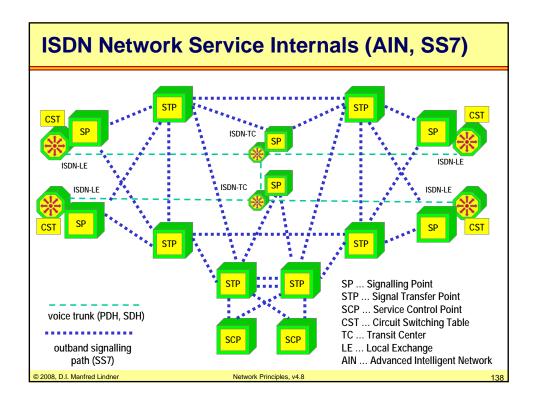


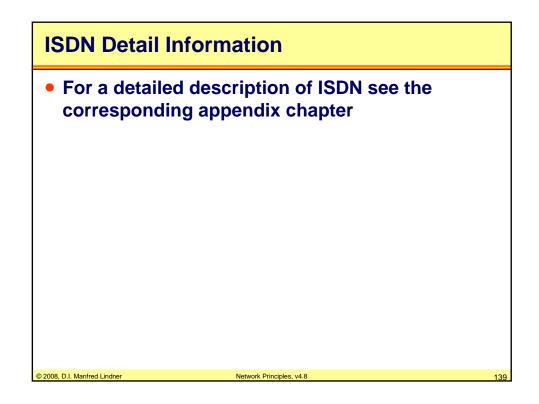
Layer 3 Messages		
<ul> <li>Call Establishment <ul> <li>ALERTing</li> <li>CALL PROCeeding</li> <li>CONNect</li> <li>CONNect ACKnowledge</li> </ul> </li> <li>SETUP <ul> <li>SETUP ACKnowledge</li> </ul> </li> <li>Call Information Phase</li> <li>RESume <ul> <li>RESume ACKnowlegde</li> <li>RESume REJect</li> </ul> </li> <li>SUSPend ACKnowledge <ul> <li>SUSPend REJect</li> <li>USER INFOrmation</li> </ul> </li> </ul>	<ul> <li>Call Clearing <ul> <li>DETatch</li> <li>DETach ACKnowledge</li> </ul> </li> <li>DISConnect <ul> <li>RELease</li> <li>RELease COMplete</li> </ul> </li> <li>REStart <ul> <li>REStart ACKnowledge</li> </ul> </li> <li>Miscellaneous <ul> <li>CANCel, - Ack, - Reject</li> <li>CONgestion CONtrol</li> <li>FACility, - Ack, - Reject</li> <li>INFOrmation</li> <li>REGister Ack, - Reject</li> <li>STATUS</li> </ul> </li> </ul>	
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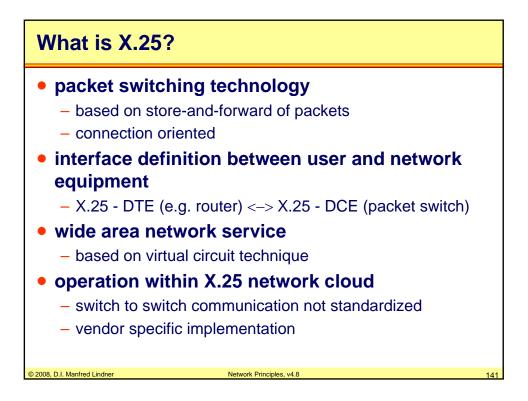


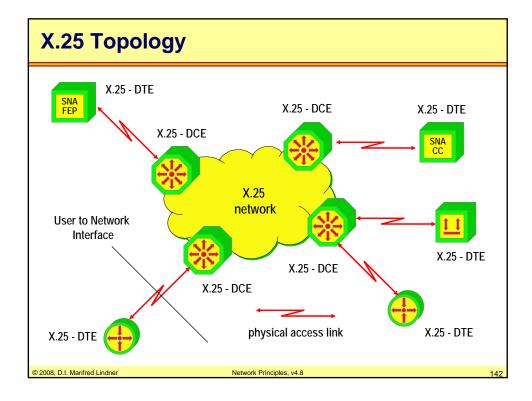


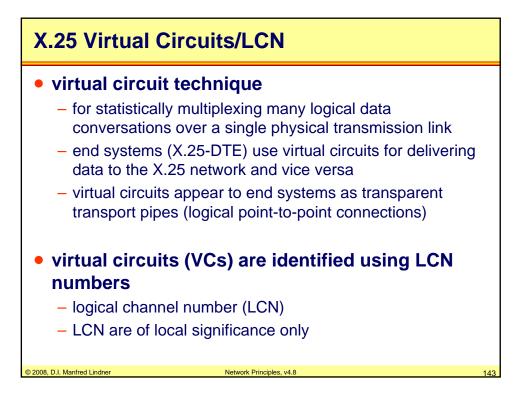


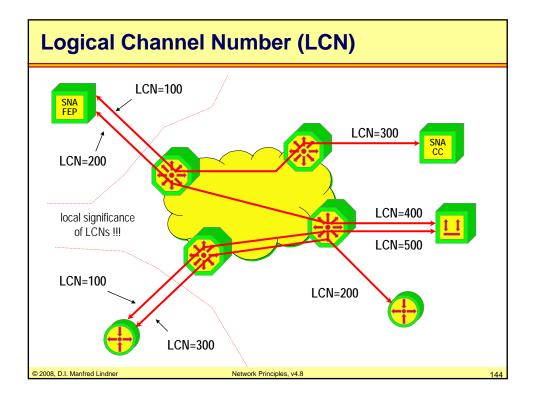


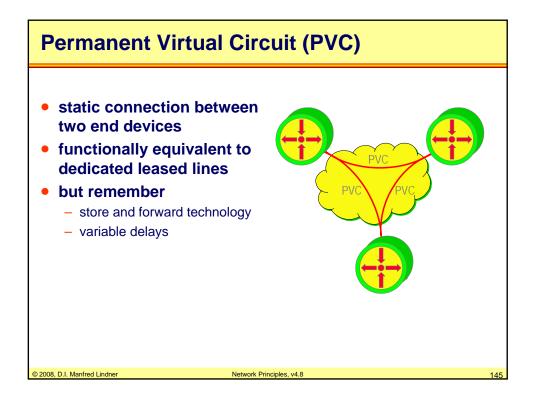
Agenda		
Introduction		
Circuit Switching		
Packet Switching		
– Principles		
<ul> <li>Datagram Service</li> </ul>		
<ul> <li>Virtual Call Service</li> </ul>		
OSI Reference Model		
<ul> <li>Summary of Network Methods</li> </ul>		
Short Introduction of		
– IDSN		
<ul> <li>X.25, Frame Relay, ATM</li> </ul>		
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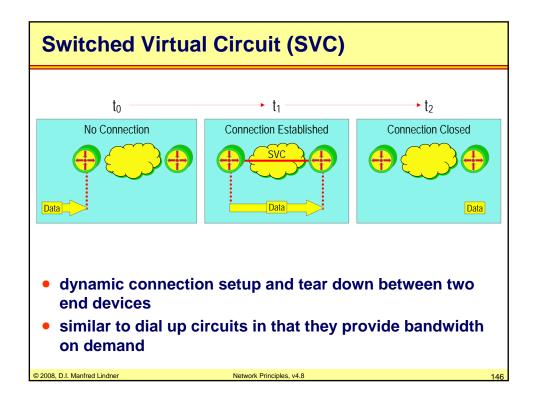


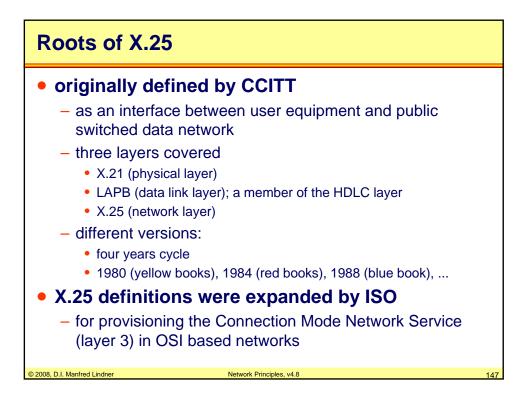


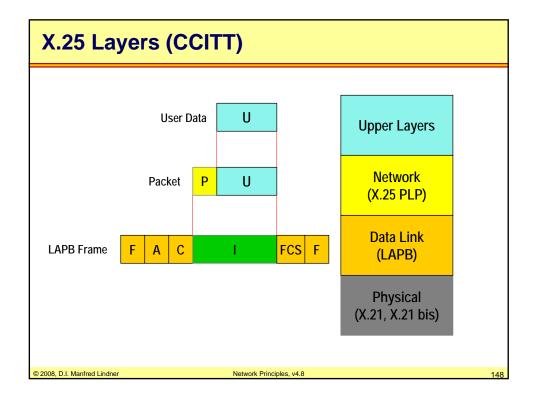


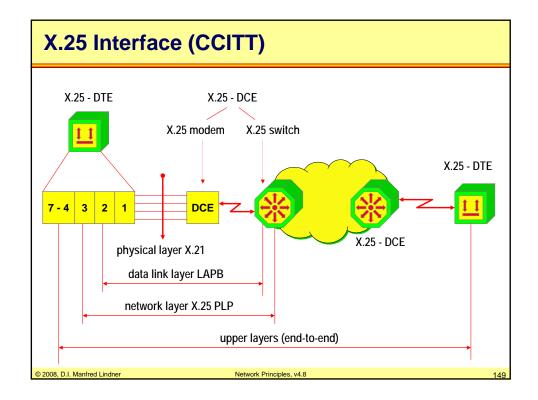




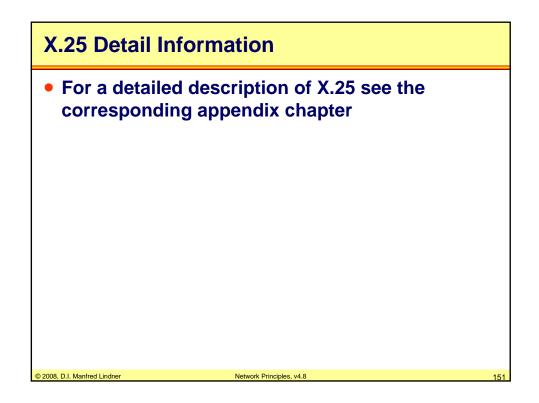


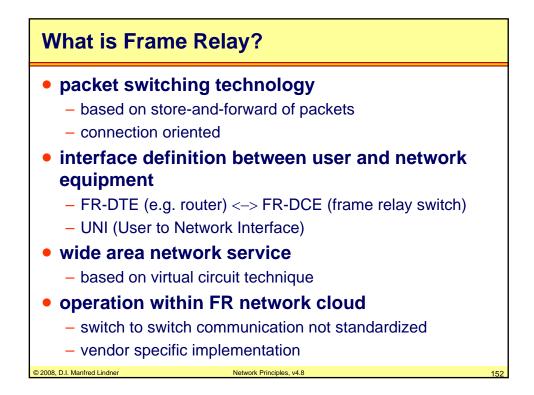


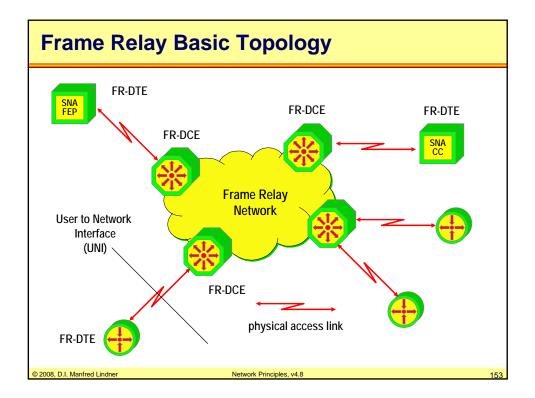


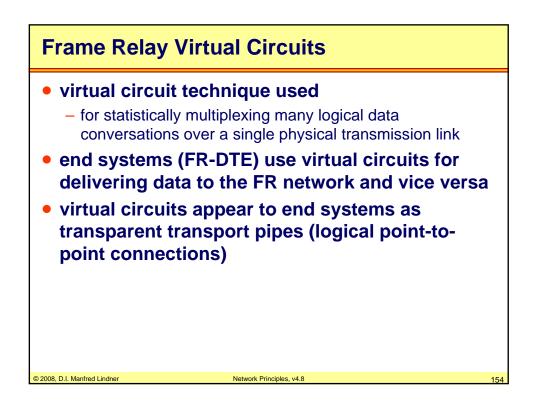


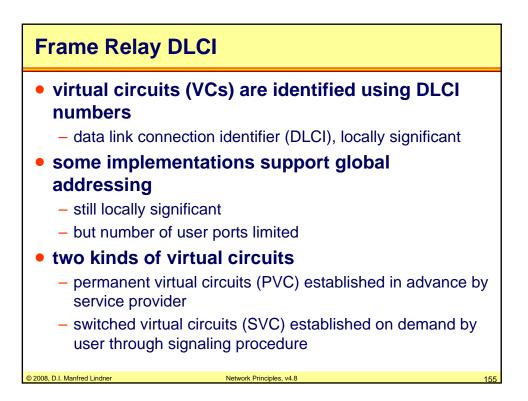
X.25 Facts
<ul> <li>remember:</li> <li>X.25 standards defines communication between DTE and DCE only</li> <li>operation (e.g. routing) within network not defined</li> <li>only sequencing must be guaranteed</li> <li>X.25 uses statistical multiplexing</li> <li>X.25 technology was developed for low quality, low speed lines</li> <li>use error recovery and flow control on layer 2 to control transmission of frames over physical line</li> <li>use flow control and optionally error recovery on layer 3 to control transmission of packets over a virtual circuit</li> </ul>
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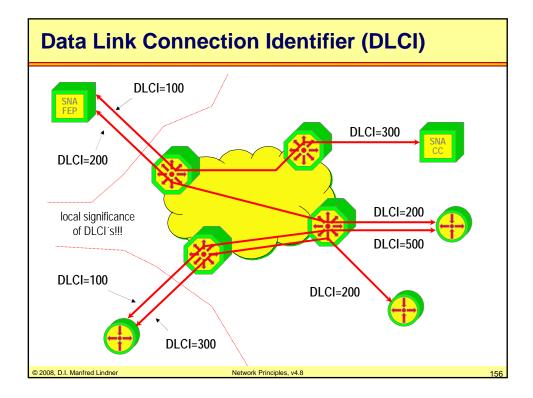


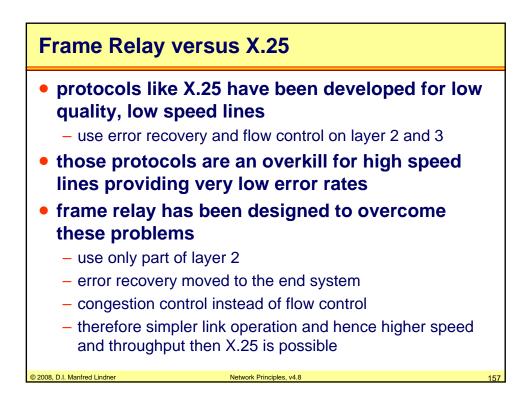


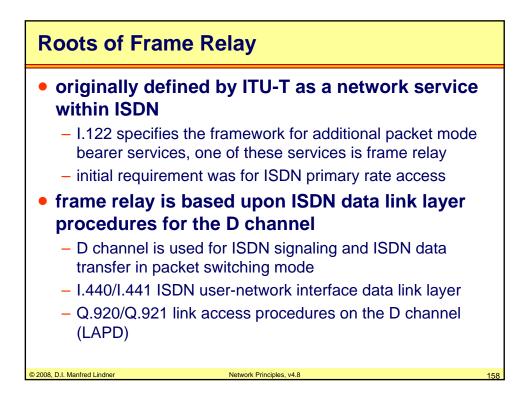


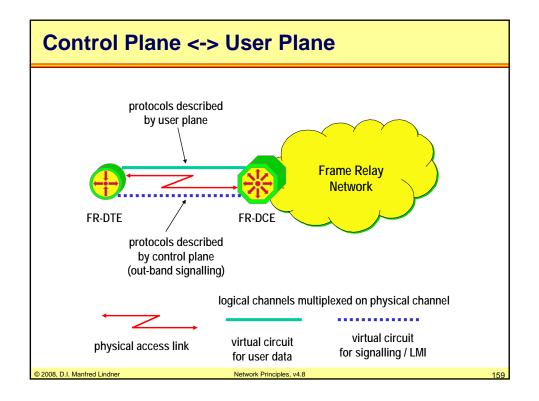


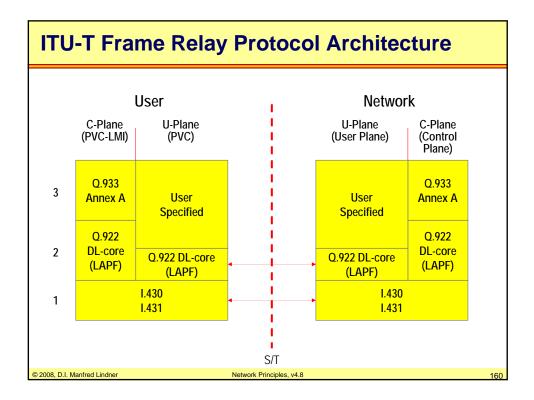


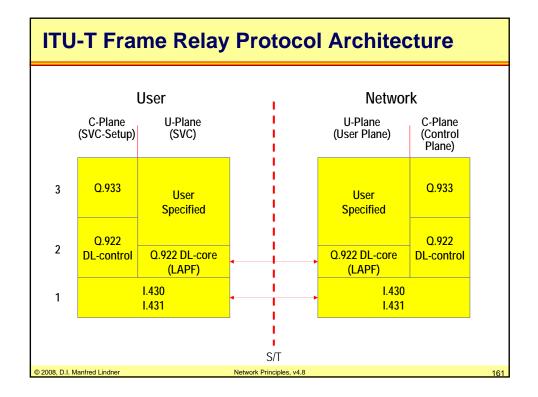


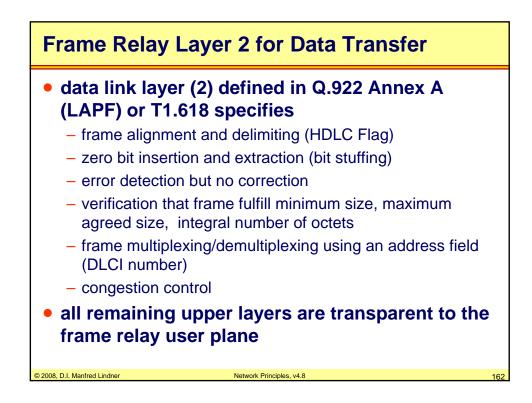


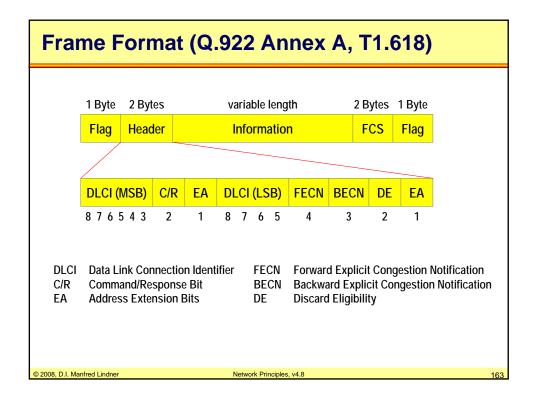


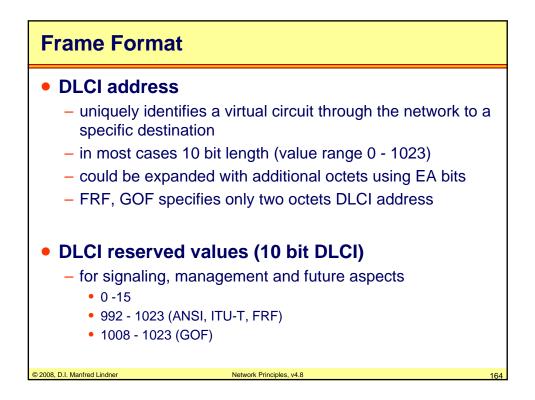


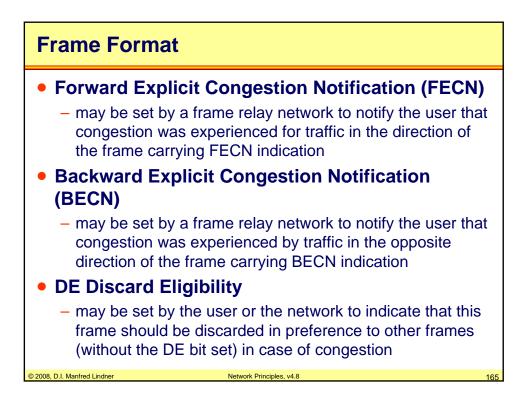


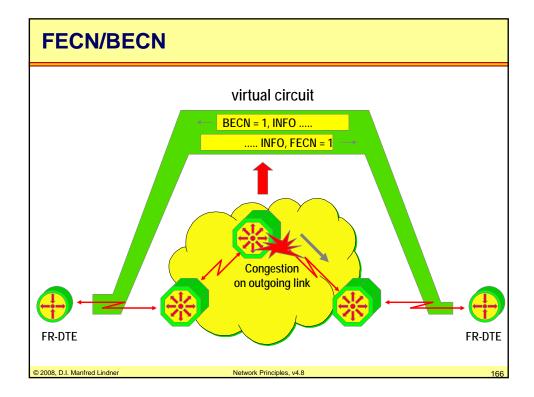


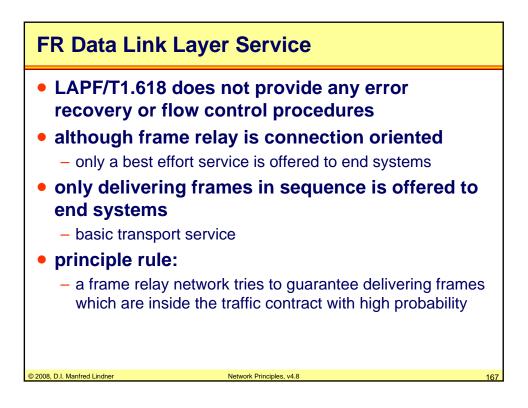


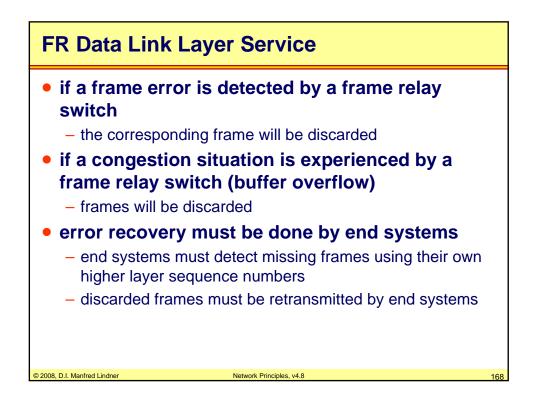


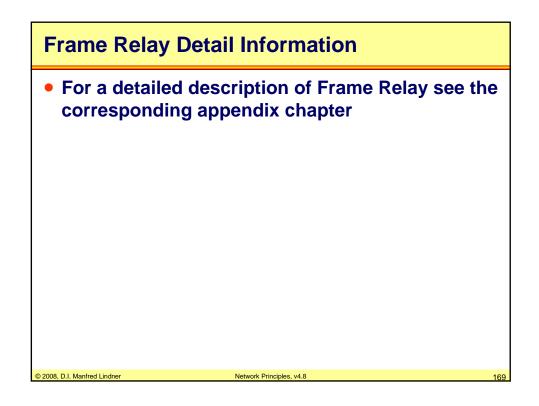


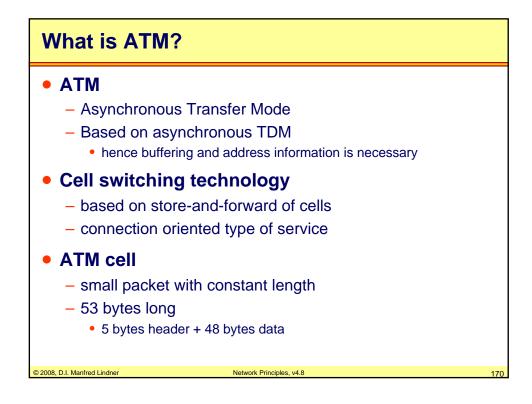


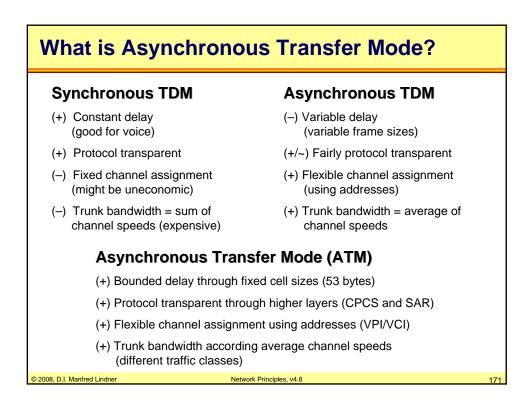


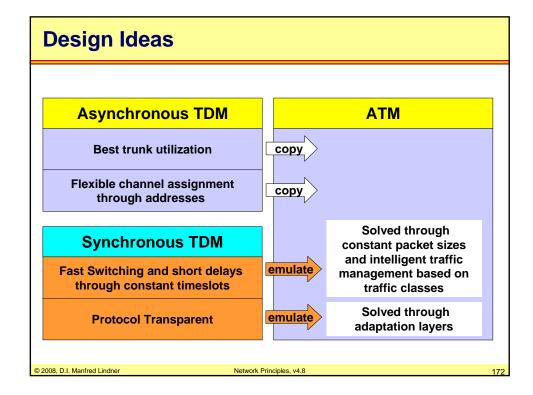


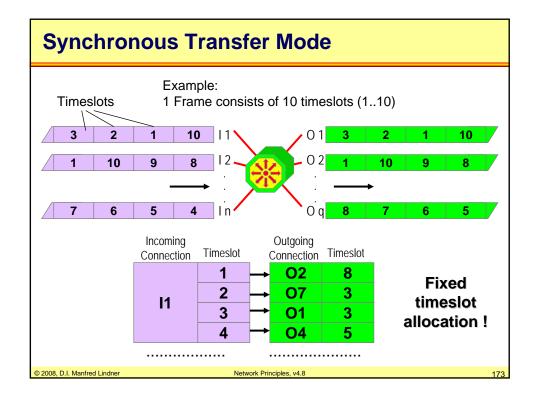


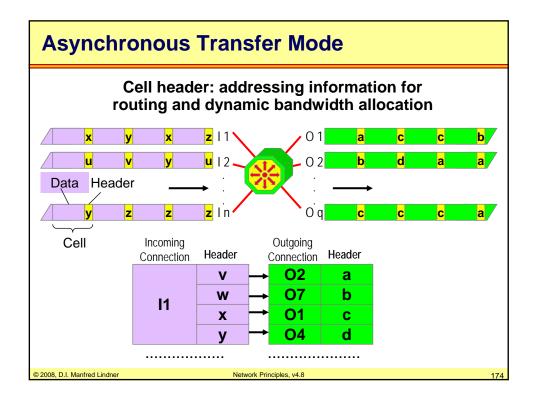


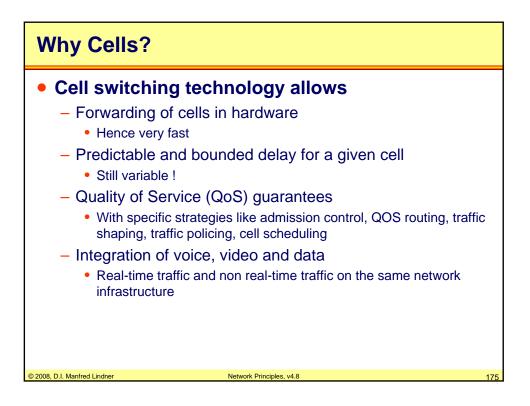


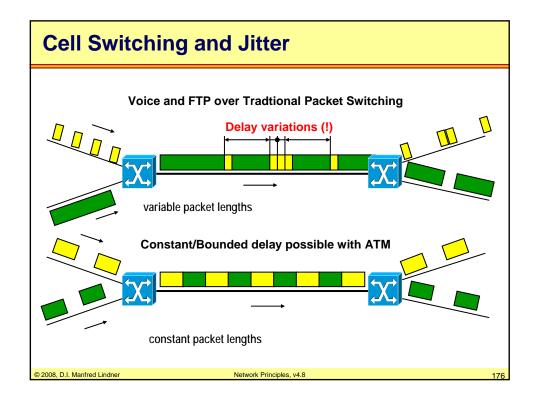


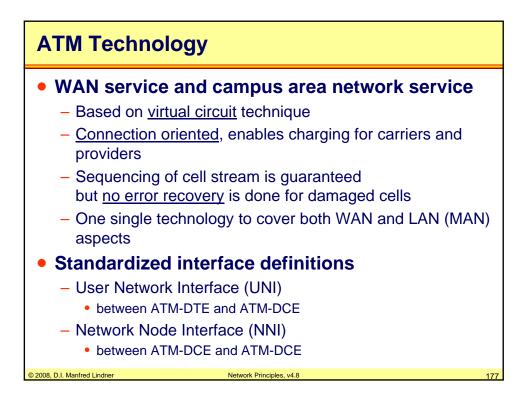


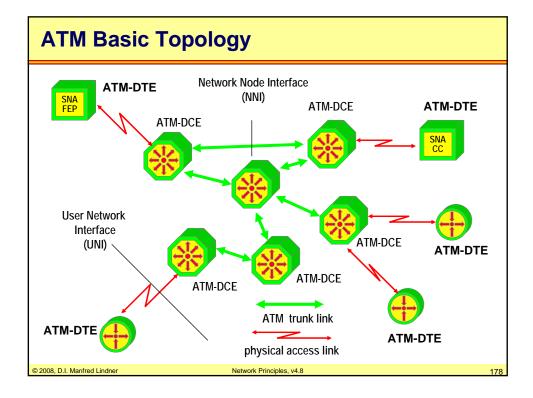


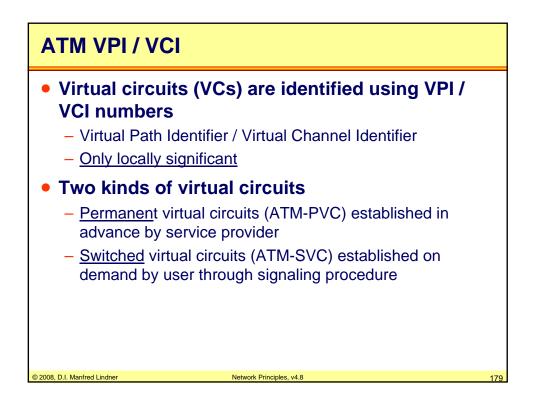


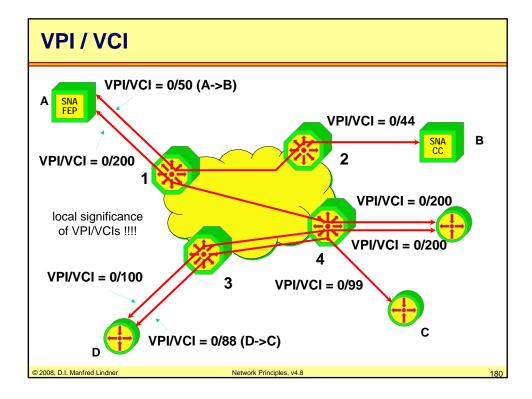


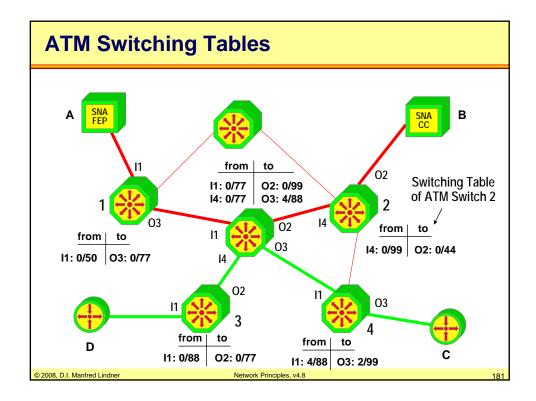


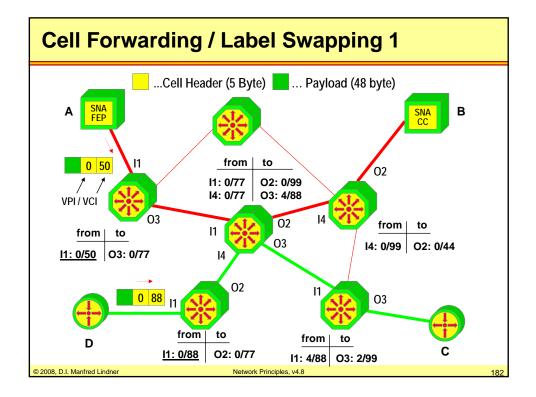


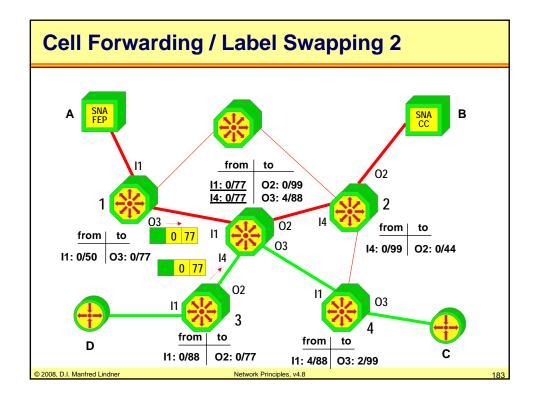


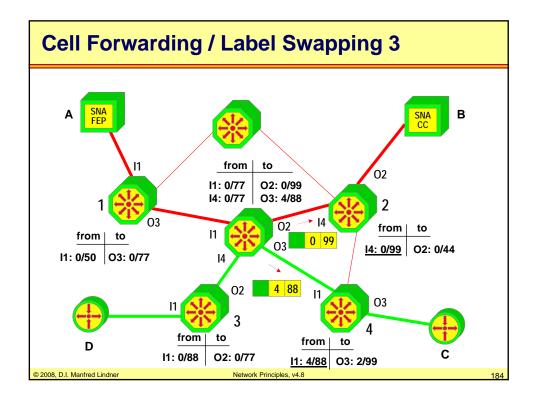


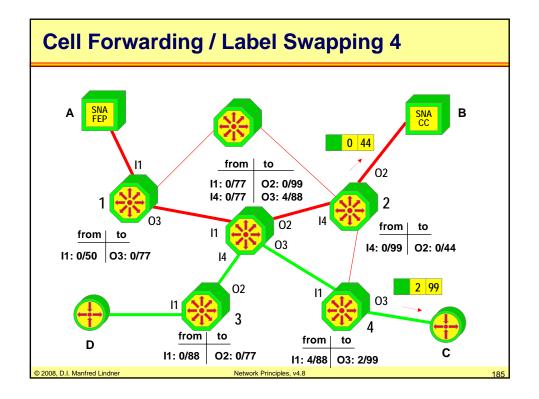


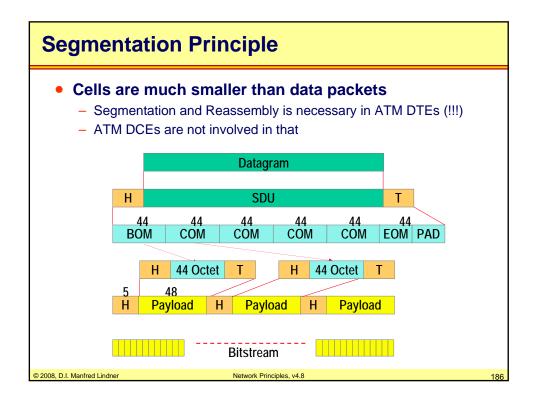


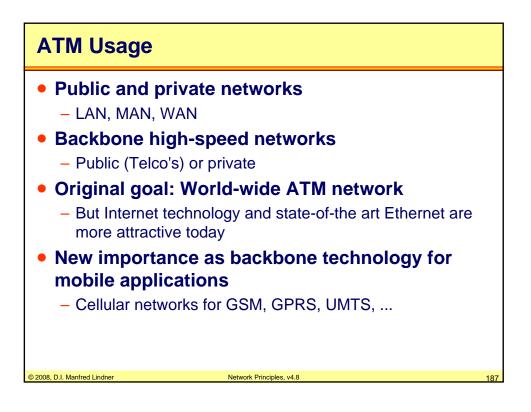


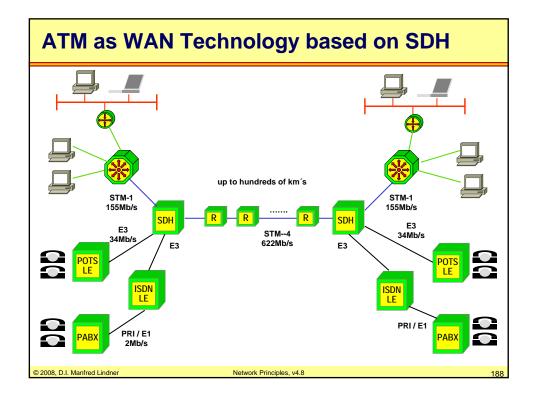


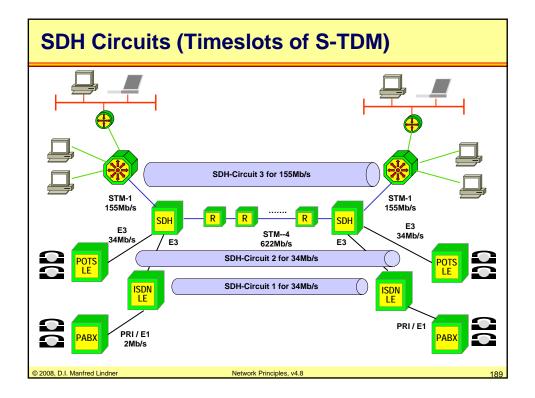


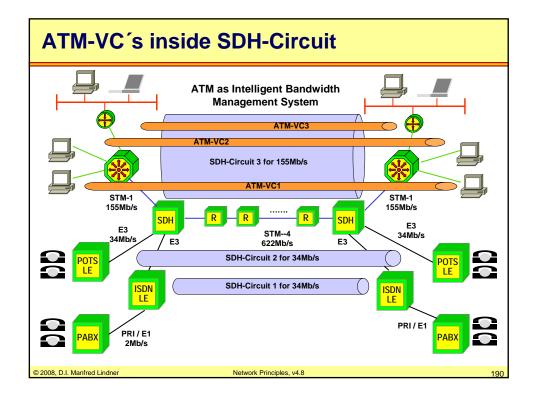


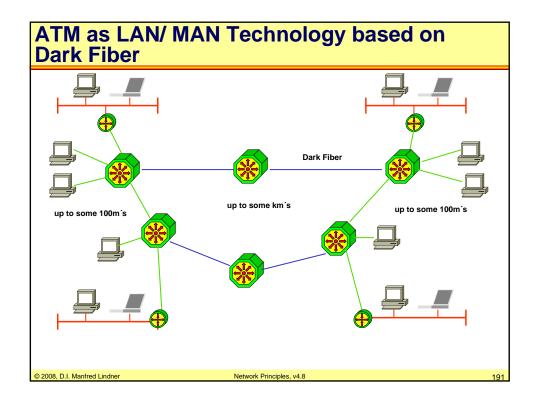




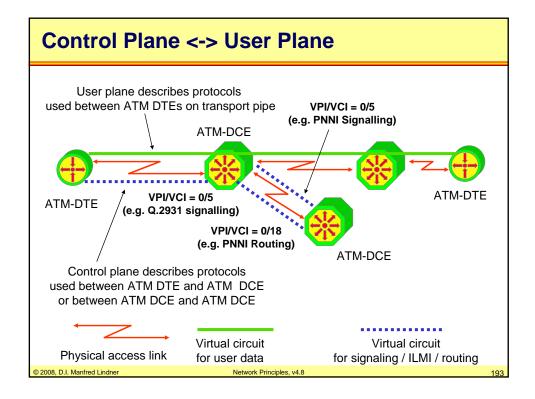


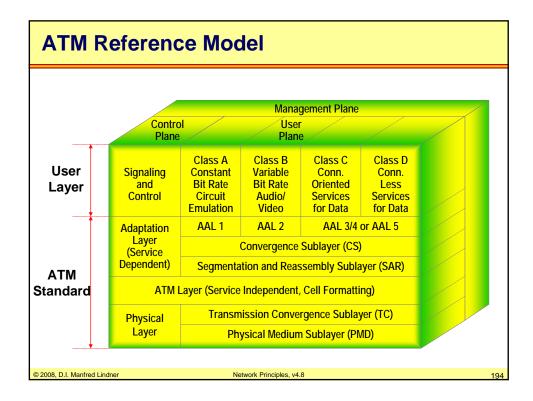


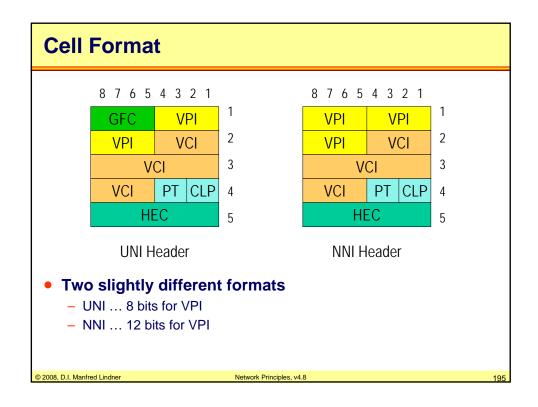




ATM Reference Model	
User Plane	
<ul> <li>Provides for transfer of information</li> </ul>	
Control Plane	
<ul> <li>Call control, connection control, PVC management, interim local management interface (e.g. ILMI)</li> </ul>	
Management Plane	
<ul> <li>Layer management</li> </ul>	
<ul> <li>e.g. meta-signaling, layer specific Operation and Maintenance (OAM) information flow</li> </ul>	
<ul> <li>– Plane management</li> </ul>	
<ul> <li>Management functions related to the whole system</li> </ul>	
<ul> <li>Coordination between all planes</li> </ul>	
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Cell Format		
• Cell Size – 53 byte		
	er and 48 byte payload	
• VPI and VCI		
– VPI - Virtual	Path Identifier	
- VCI - Virtual	Channel Identifier	
– VPI/VCI iden	ntifies the virtual connection	
<ul> <li>Similar function</li> <li>Relay DLCI</li> </ul>	tion as the X.25 logical channel identifie	er or the Frame
<ul> <li>Reserved va</li> </ul>	lues used for	
<ul> <li>Signaling</li> </ul>		
<ul> <li>Operation a</li> </ul>	and maintenance	
Resource m	nanagement	
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VPI	VCI	Function			
411					
0	0- 15	ITU-T			
0	16 - 31	ATM Forum			
0	0	Idle Cell			
0	3	Segment OAM Cell (F4)			
0	4	End-to-End OAM Cell (F4)			
0	5	Signaling			
0	16	ILMI			
0	17	LANE			
0	18	PNNI			

